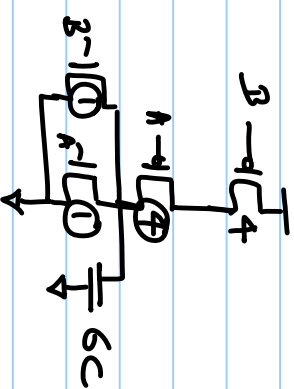
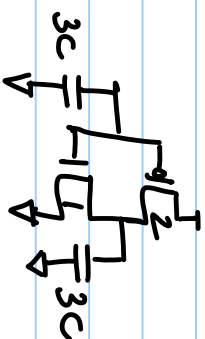
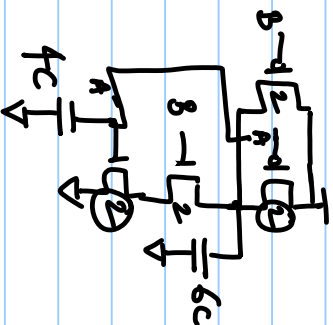


27/09/2019

EE5311

Module-4 - Comb CKTS



$$g = \frac{4}{3} \quad (\text{LOGICAL EFFORT})$$

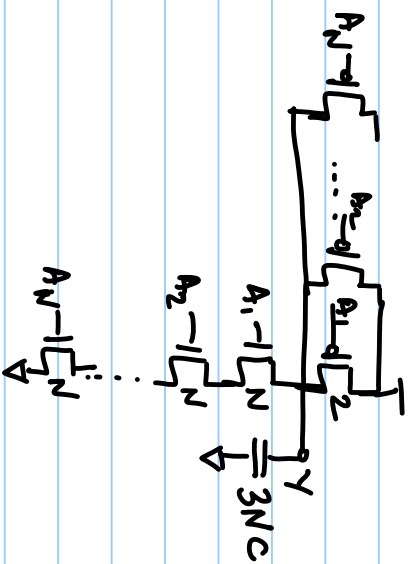
$$p = \frac{6C}{3C} = 2 \quad (\text{PARASITIC EFFORT})$$

$$C_A = C_B = 5C$$

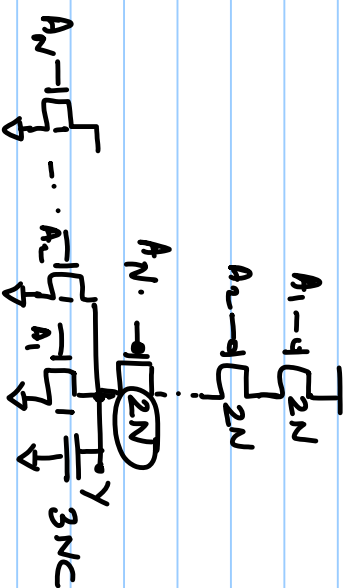
$$g_A = g_B = 5C/3C = 5/3$$

$$p = 6C/3C = 2$$

N input NAND & NOR.



$$C_{AK} = (N+2)C$$



$$C_{AK} = (2N+1)C$$

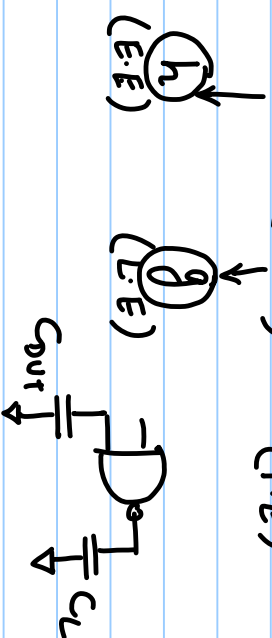
$$\therefore g_{AK} = \frac{(N+2)C}{3C} = \left(\frac{N+2}{3}\right)$$

$$g_{AK} = \frac{(2N+1)C}{3C} = \left(\frac{2N+1}{3}\right)$$

$$p = \frac{3NC}{3C} = N$$

$$p = \frac{3NC}{3C} = N$$

$$\text{delay} = \left(\frac{C_L}{C_{DUT}} \right) \cdot \left(\frac{C_{UNIT}}{C_{INV}} \right) + p. \quad (P.E)$$

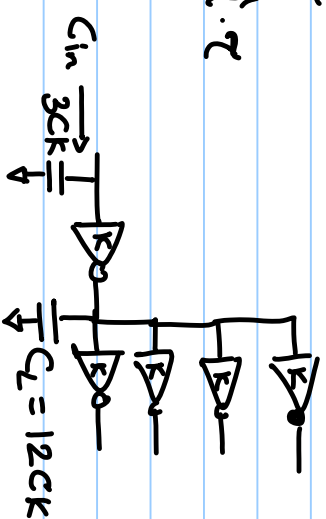


$$\hat{d} = g_h + p. \quad (\text{NORM. DELAY})$$

$$\tau = 3RC$$

$$d = \hat{d} \cdot \tau$$

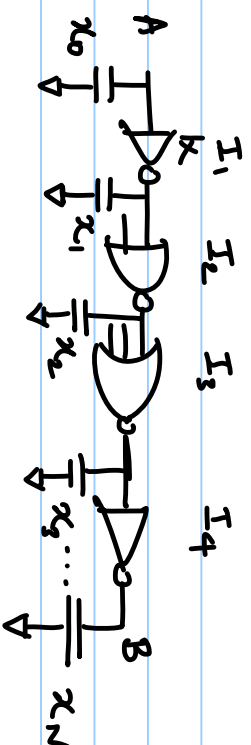
$$h = 12KC/3KC = 4$$



$$h = C_L / C_{in} = 12C/3C = 4$$



PATH DELAY OPTIMIZATION



MINIMIZE DELAY \forall A 2 B.

Let $x_k \rightarrow$ Input cap of gate $(k+1)$

$$d_i = g_i h_i + p_i$$

$$h_1 = x_1 / x_0.$$

$$h_k = (x_k / x_{k-1})$$

$$d = \sum g_k h_k + p_k.$$

$$= \sum g_k h_k + \underbrace{\sum p_k}_{\text{constant number}} = P = P_{\text{PATH}} \quad P_{\text{PARASITIC EFFORT}}$$

$$\min \left(\sum_{k=1}^N g_k \left(\frac{x_k}{x_{k-1}} \right) \right)$$

$$F = \prod_{k=1}^N g_k h_k. =$$

$$\underbrace{\prod_{k=1}^N g_k}_G \cdot \underbrace{\prod_{k=1}^N h_k}_H$$

$$\rightarrow \frac{x_1}{x_0} \cdot \frac{x_2}{x_1} \cdot \frac{x_3}{x_2} \cdots \frac{x_N}{x_{N-1}}$$

$$= \frac{x_N}{x_0}$$

$$\text{Let } f_k = g_k \cdot h_k. \quad \min \sum f_k. \quad \prod f_k = \text{const}$$

$$\frac{\sum f_k}{N} \geq (\prod f_k)^{1/N}$$

$$A.M \geq G.M$$