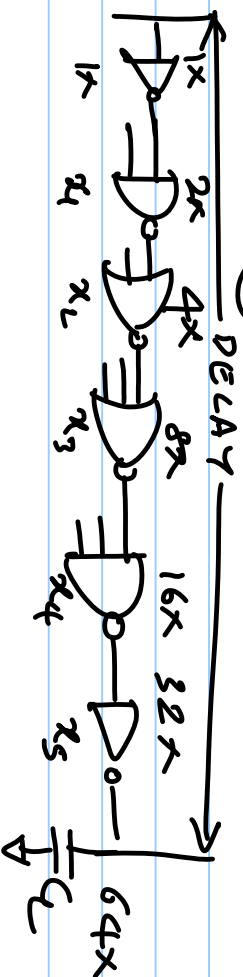
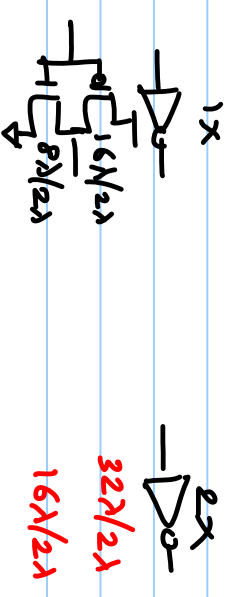
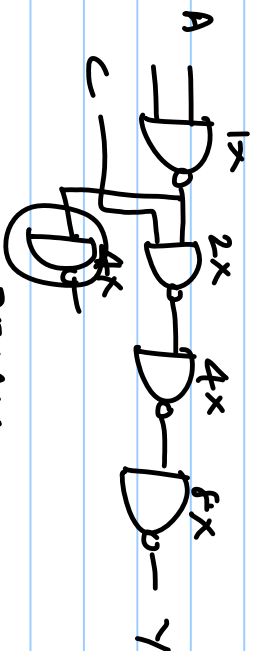
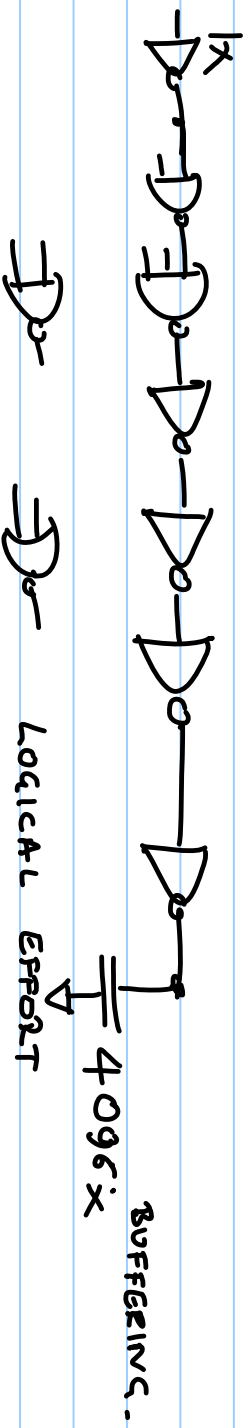


ESTIMATING GATE DELAY

ESTIMATING PATH DELAY

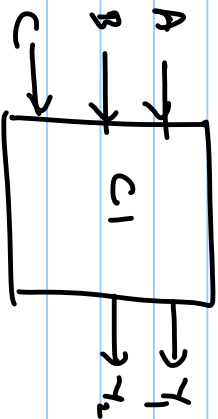


GATE DELAY OPTIMIZATION



4096x
BUFFERING.

BASICS



A	B	C	Y_1	Y_2
0	0	0	0	0
0	0	1	0	1
0	1	0	1	1
1	1	1	1	1

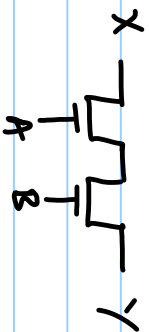
$$Y_1 = \sum m(0,3,5) \rightarrow \text{sum of products.}$$

$$Y_2 = \sum m(1,2,7)$$

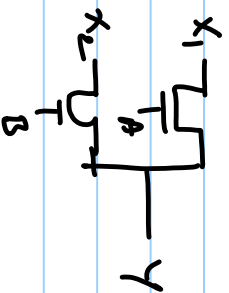
$$Y_1 = \bar{A}\bar{B}\bar{C} + \bar{A}BC + A\bar{B}C$$

OR

Active High switches

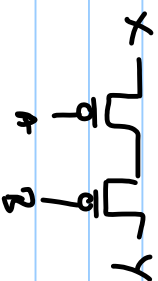


$$Y = AB \cdot X$$

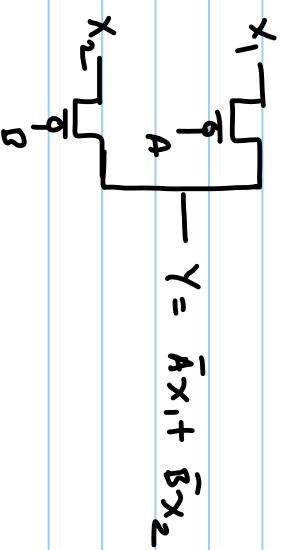


$$Y = AX_1 + BX_2$$

Active Low switches



$$Y = \overline{AB} \cdot X$$



$$Y = \overline{A}X_1 + \overline{B}X_2$$