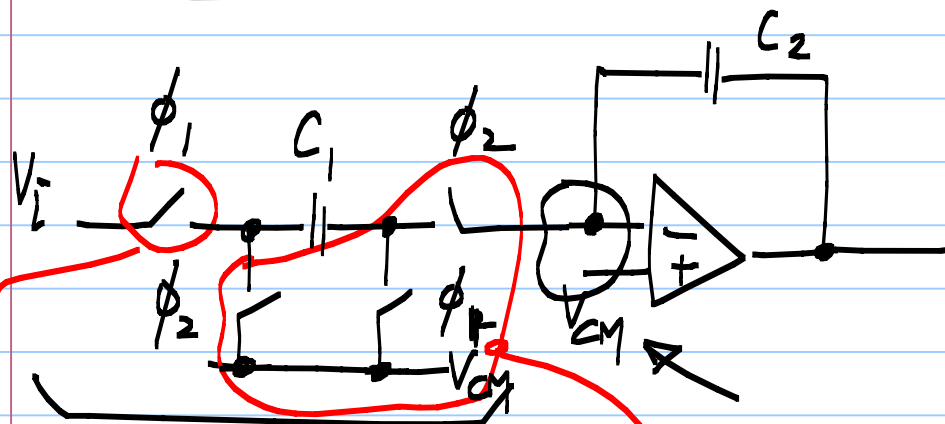


Lecture 60:

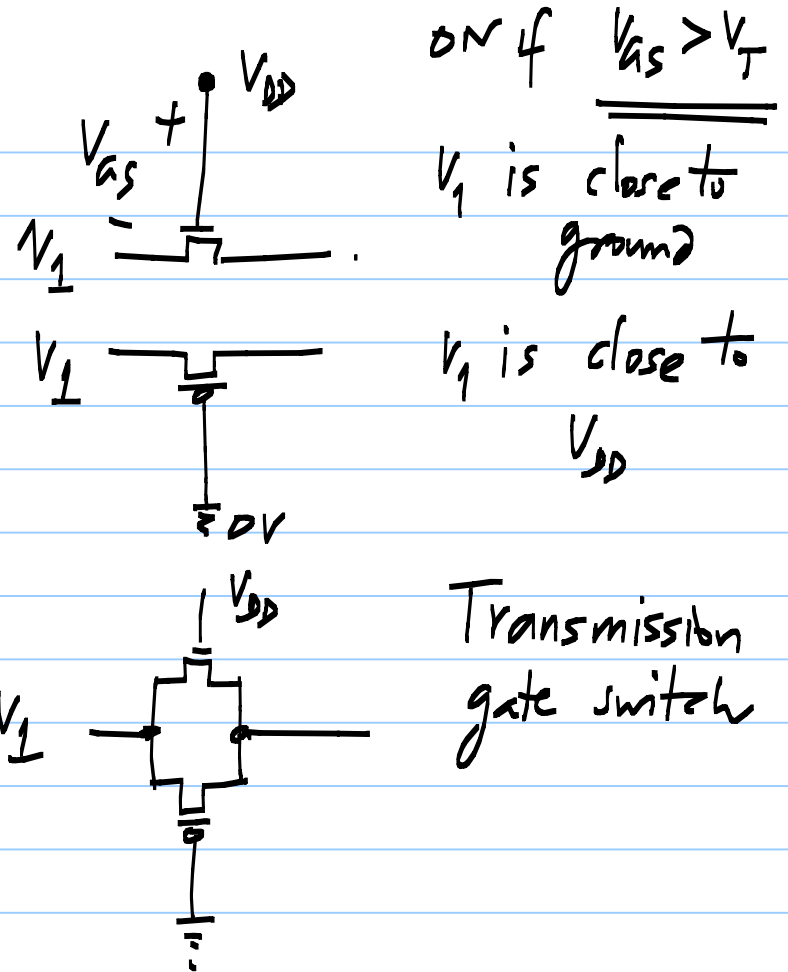
Switched capacitor filters:

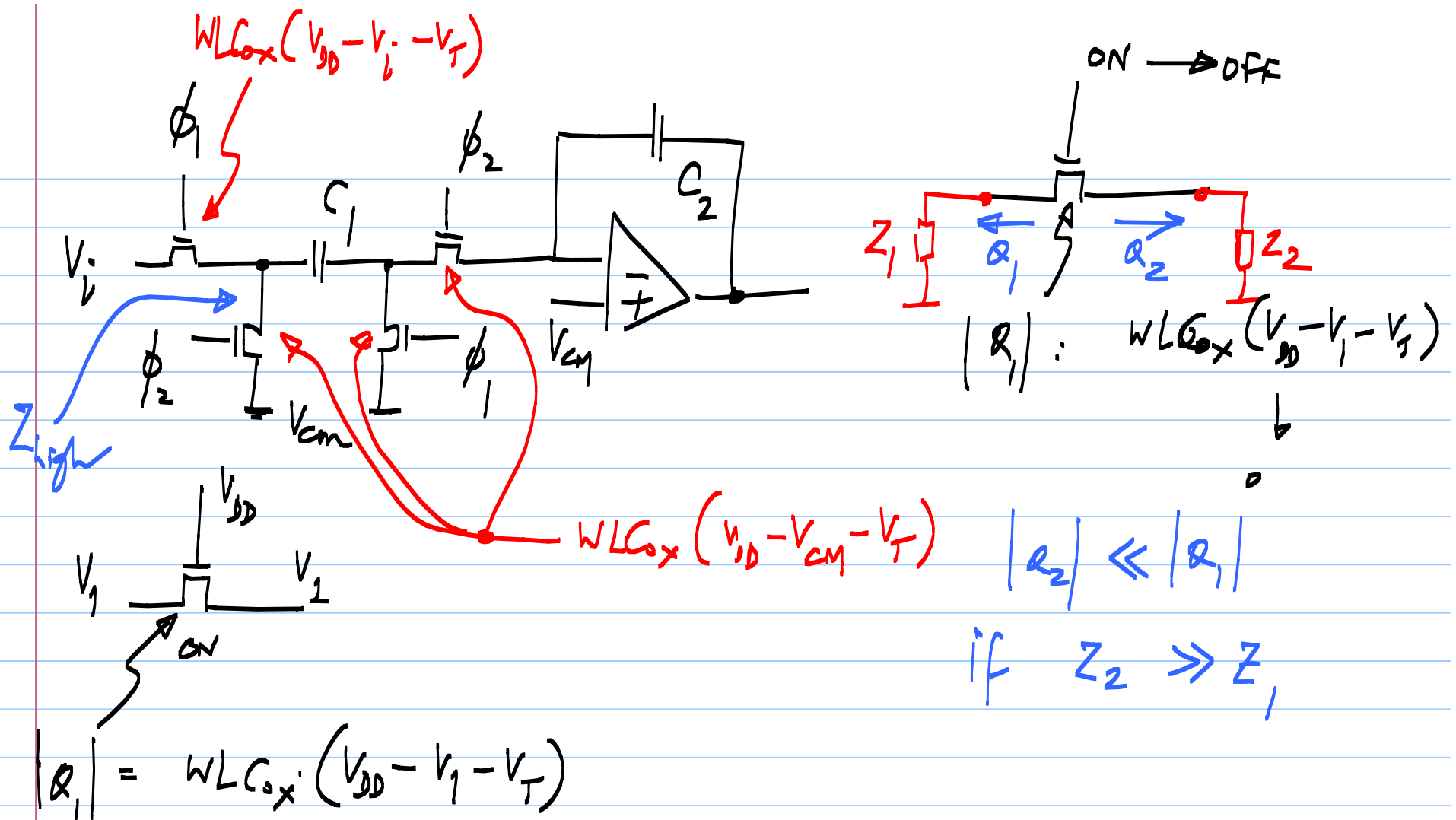


MOS transistor switches.

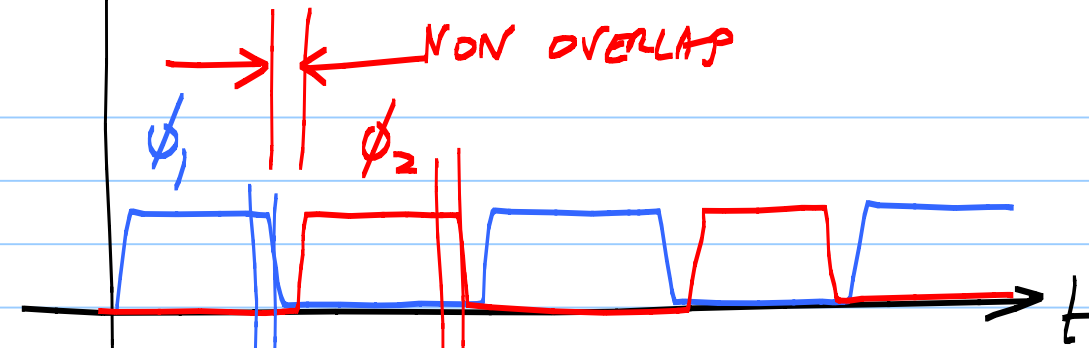
Transmission gate

Single channel

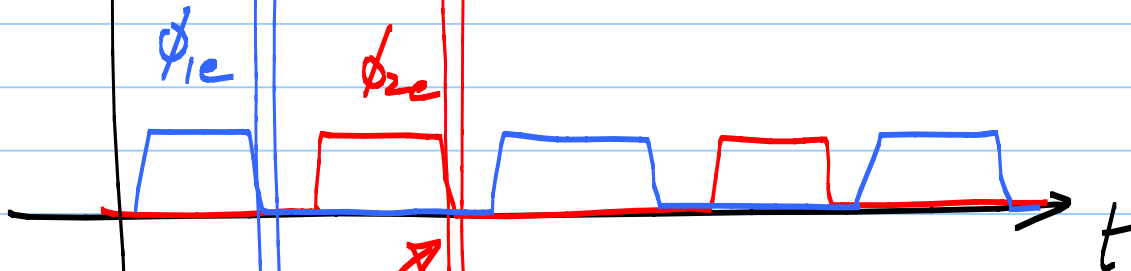




SC filter switch phases



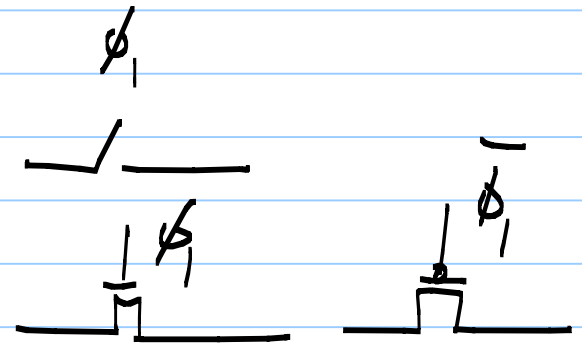
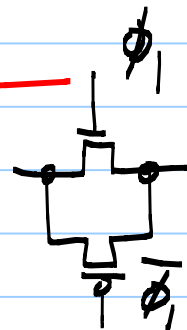
input side switches



virtual ground switches

ϕ_{1c} turns off before ϕ_2

ϕ_{2c} before ϕ_2



choosing transistor sizes:

Square law model: $I_D = \frac{\mu C_{ox}}{2} \frac{W}{L} (V_{ds} - V_T)^2$

$I_D \propto W$ $W \gg W_{min}$

$I_D \propto \frac{1}{L}$ X

I_D increases with reducing L

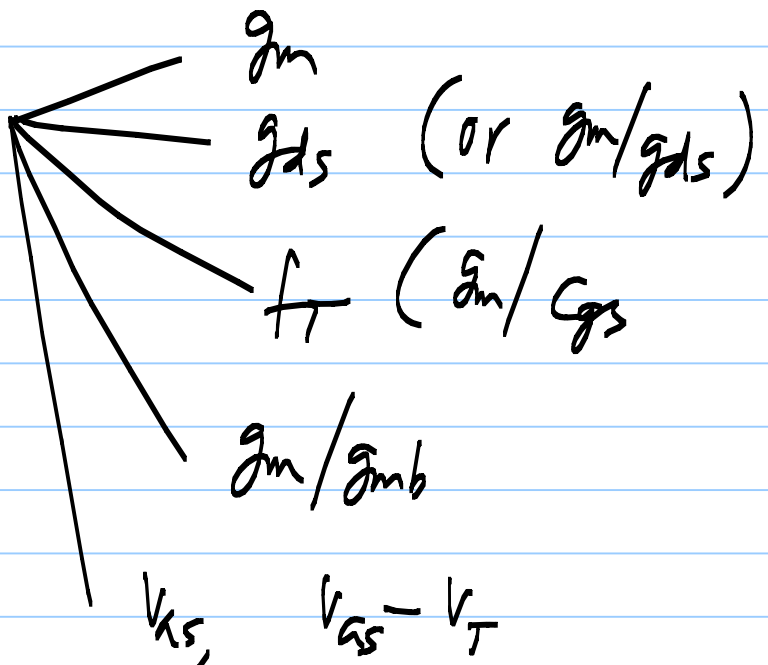
Oversimplified

not really true @ short L

* Choose a channel length L [e.g. L_{min}]

$I_D \propto \left(\frac{W}{L}\right)$ $\{W, L\}$: separate parameters

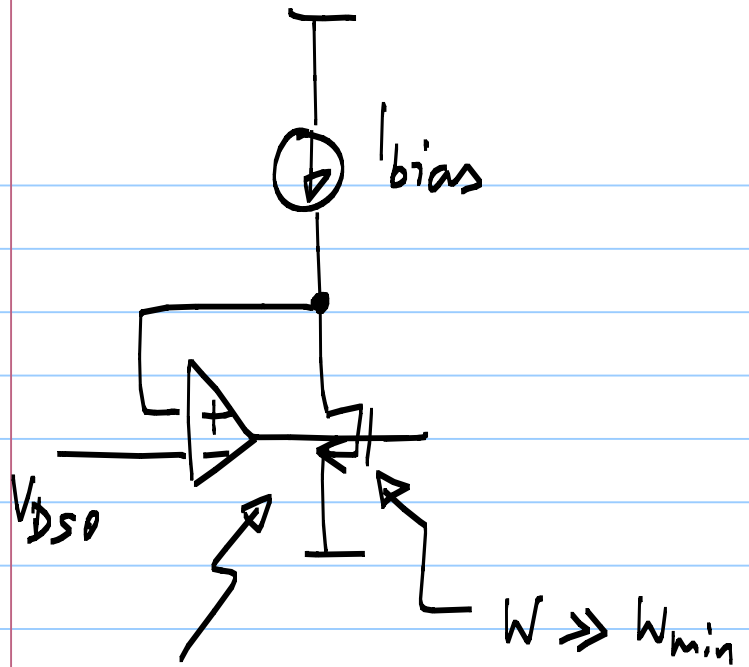
* Bias at a given I_D



- g_m
- g_{ds} (or g_m/g_{ds})
- f_T (g_m/C_{gs})
- g_m/g_{mb}
- $V_{GS}, V_{GS} - V_T$

* Vary I_D over some range & plot these parameters

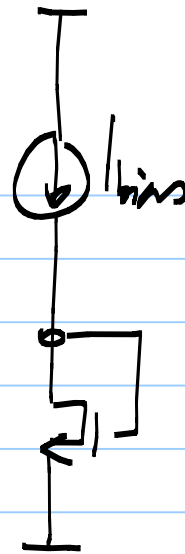
* Repeat for different L .



$$I_D = I_{bias}$$

$$V_{DS} = V_{DS0} = 0.3V$$

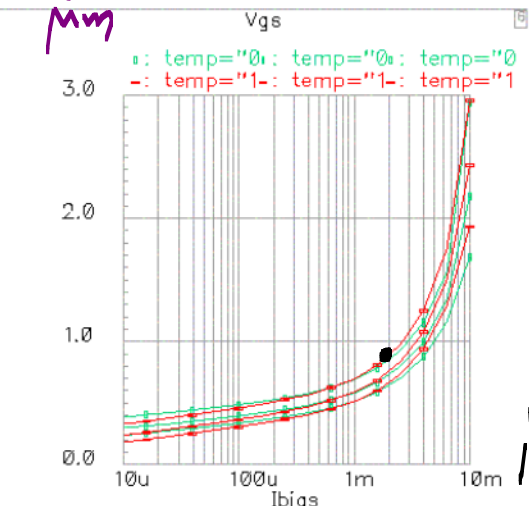
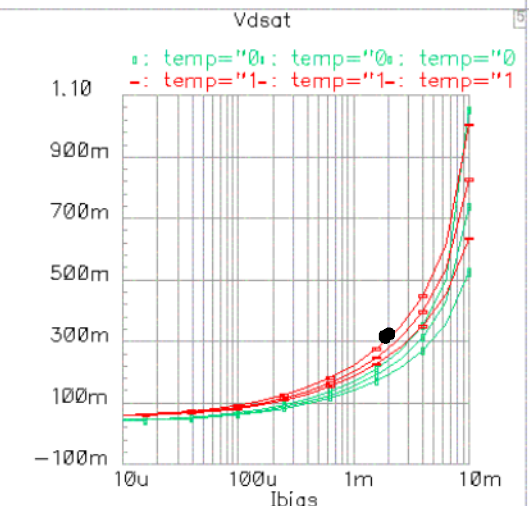
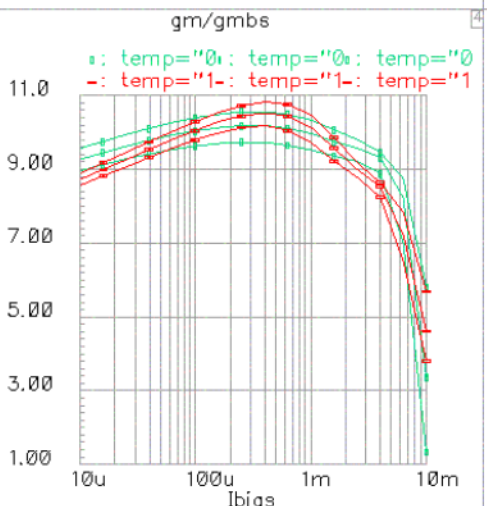
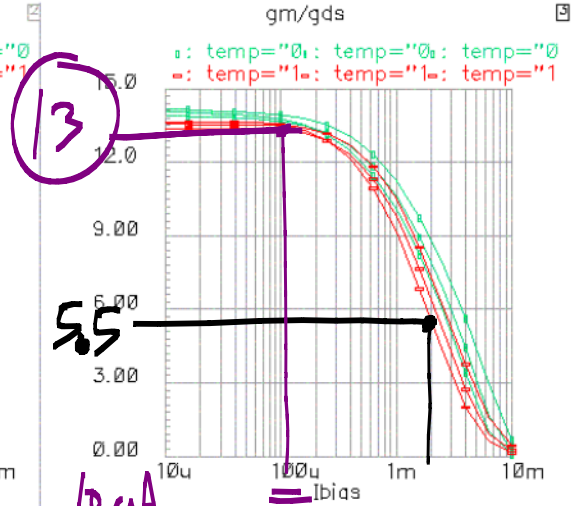
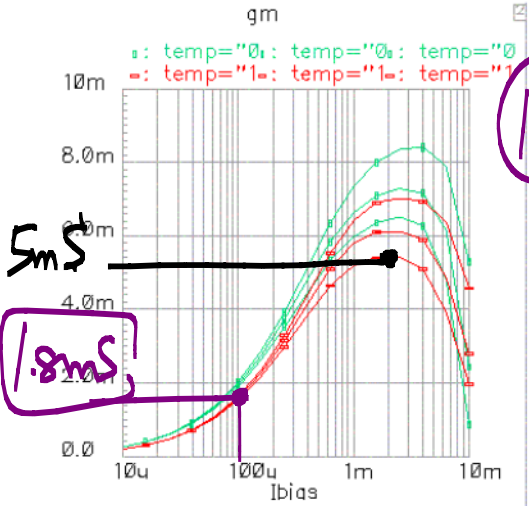
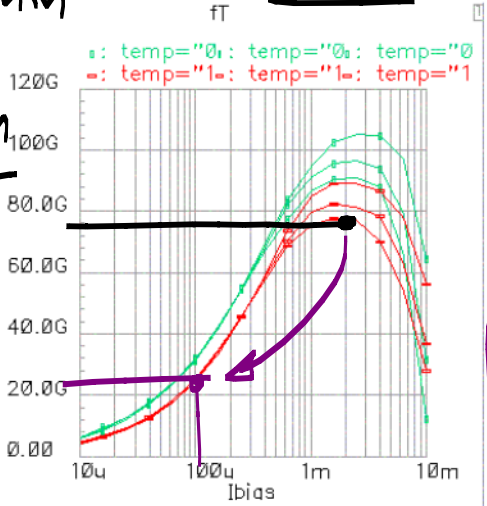
Plot vs. $\left[\frac{I_{bias}}{W} \right]$



From the operating point info, get $g_m, g_{ds} \left[\frac{g_m}{g_{ds}} \right]$
 $\frac{g_m}{g_{mb}}, \frac{g_m}{g_{gs}} \sim f_T$
 $V_{as}, V_{as} - V_T$

$L = L_{min}$
 $= 0.12 \mu m$

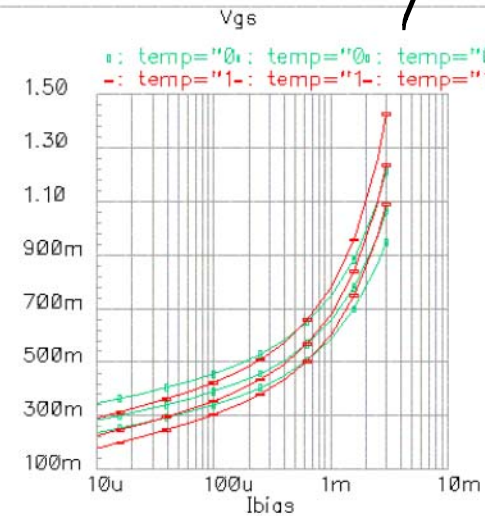
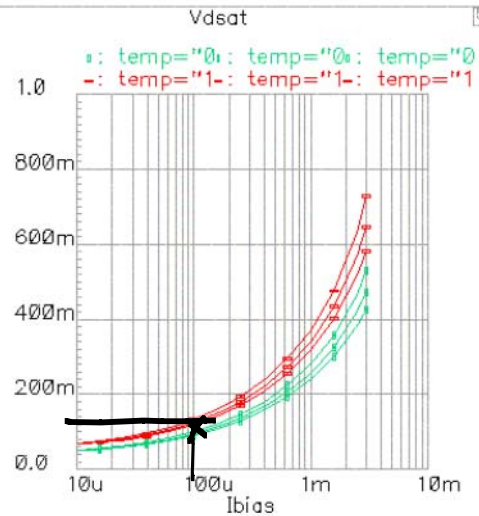
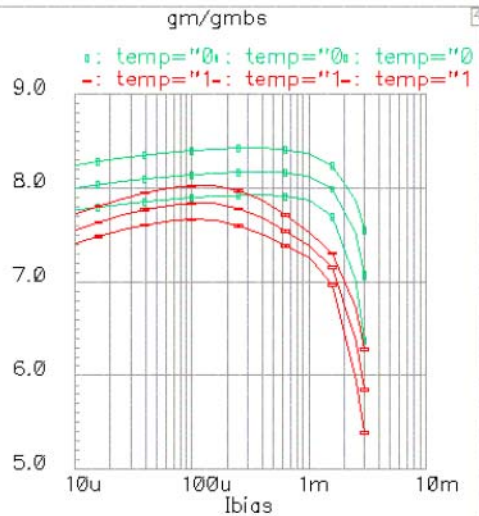
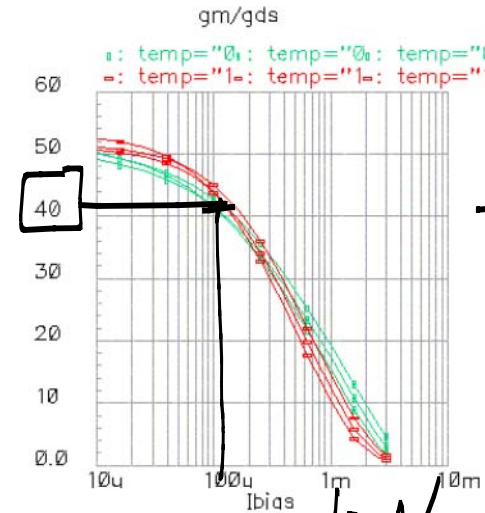
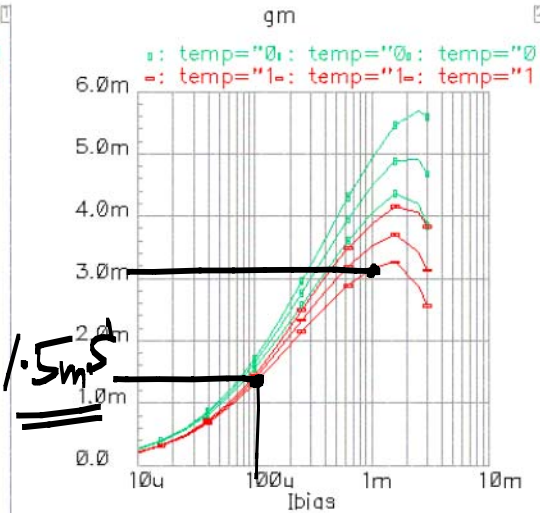
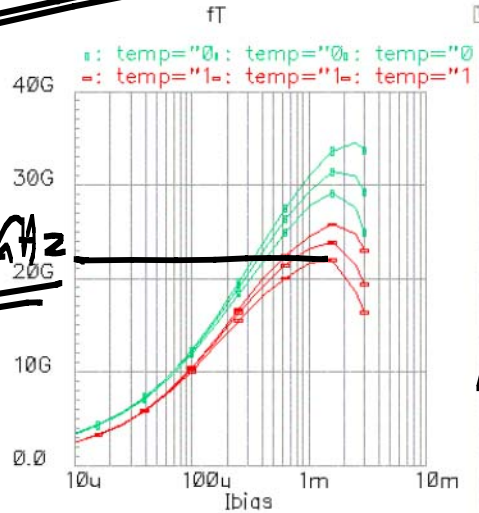
nMOS 20*0.5u/0.12u: VDS=500mV, VSB=0, variations with process(tt,ff,ss) and temperature(0,100)



$\frac{I_{bias}}{I_{op}}$
 $\frac{2mA}{(10\mu)}$
 $\left[\frac{200\mu A}{micron} \right]$
 $\frac{500\mu S}{micron}$
 $\frac{5mS}{micron}$
 $\frac{100\mu; 20mA}{(W)}$

$L = 0.25 \mu\text{m}$

nMOS 20*0.5u/0.25u: VDS=500mV, VSB=0, variations with process(tt,ff,ss) and temperature(0,100)



$\frac{1.5\text{mS}}{10\mu\text{A} (\text{W})}$

$150\mu\text{S}$

micron

15mS

$10\mu\text{A}/\text{micron}$

$\times 100x$

$100\mu\text{m}$

$1\mu\text{A}, I_0$

$L = 0.9 \mu m$

nMOS 20*0.5u/0.5u: VDS=500mV, VSB=0, variations with process(tt,ff,ss) and temperature(0,100)

