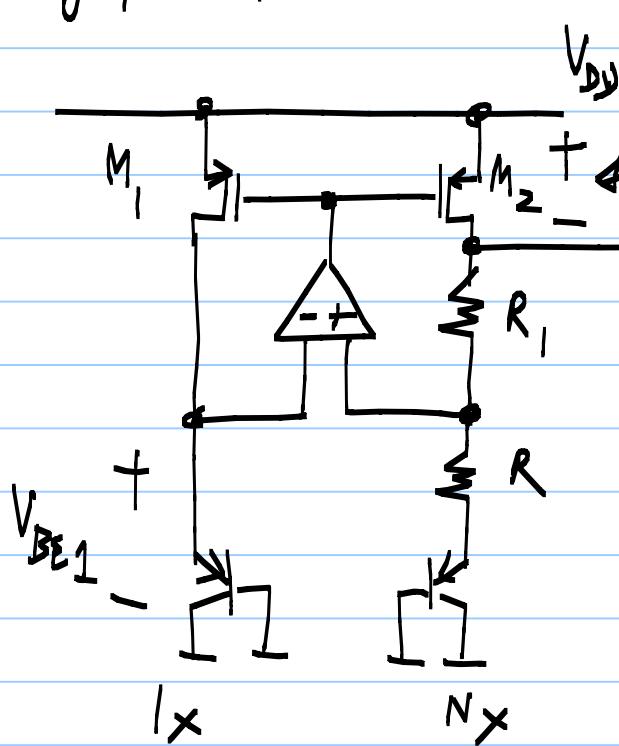


Lecture 55

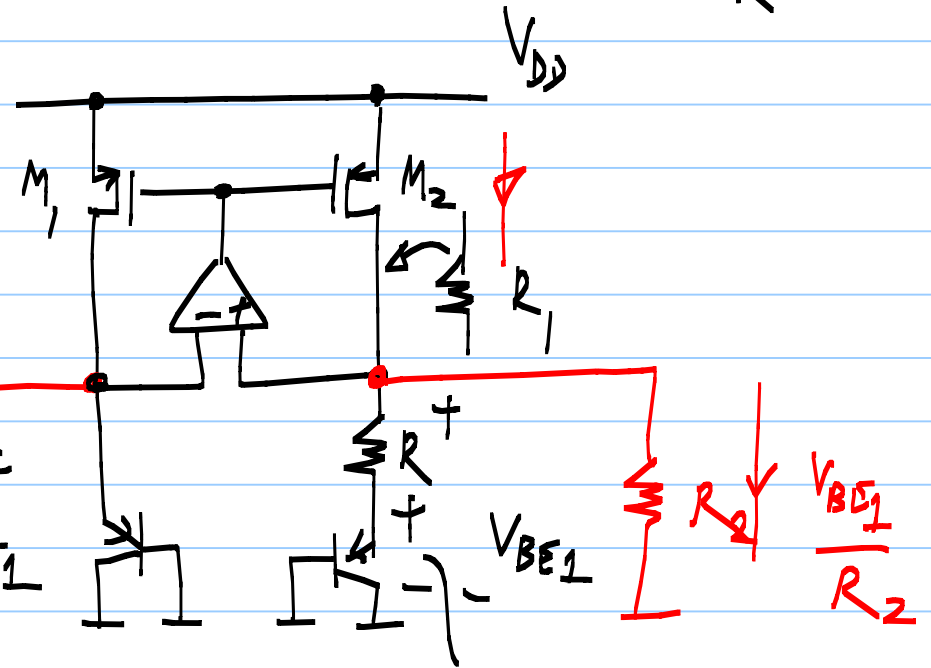
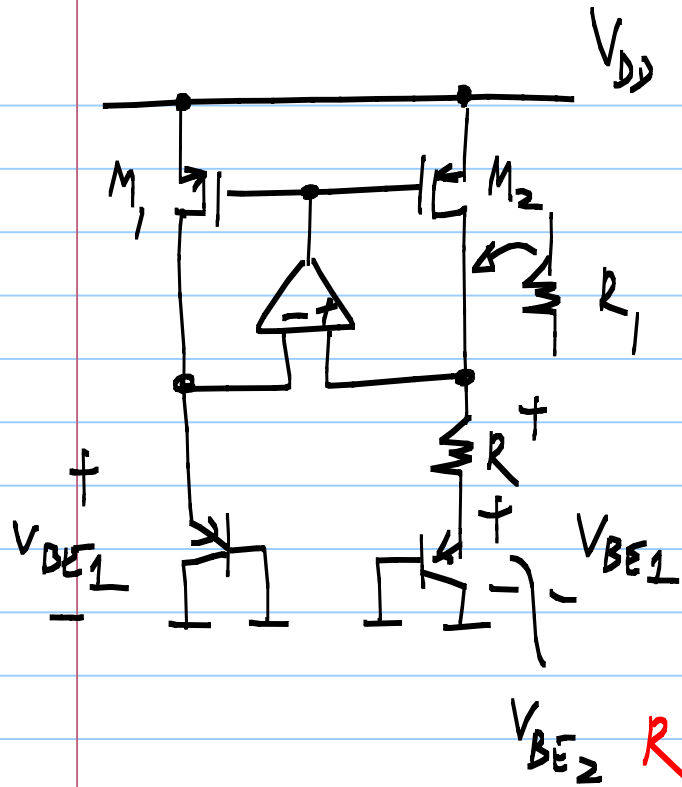
Bandgap reference



$$V_{DD, min} = V_{out} + V_{SAT, M_2}$$
$$= 1.2V + 0.2V = 1.4V$$

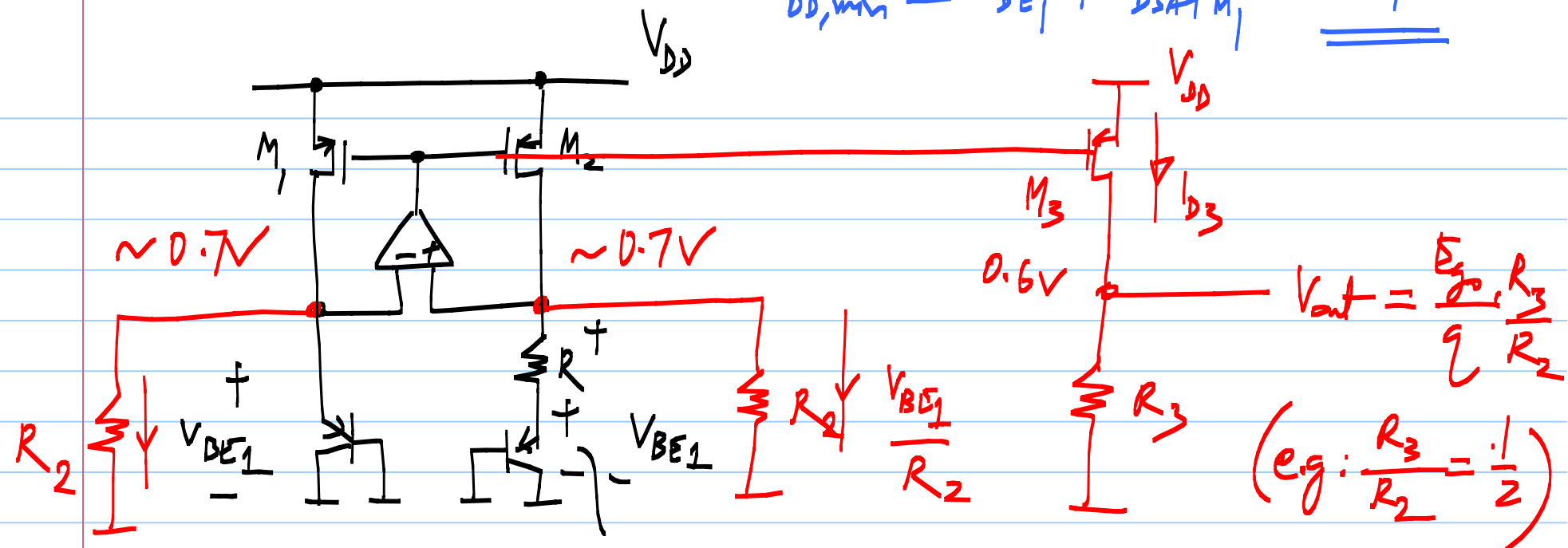
$$V_{BE1} + V_T \ln(N) \cdot \frac{R_1}{R} = \frac{E_{g0}}{q} \approx 1.2V$$

Currents in $M_{1,2}$: PTAT
 $\left(V_T \frac{\ln N}{R} \right)$



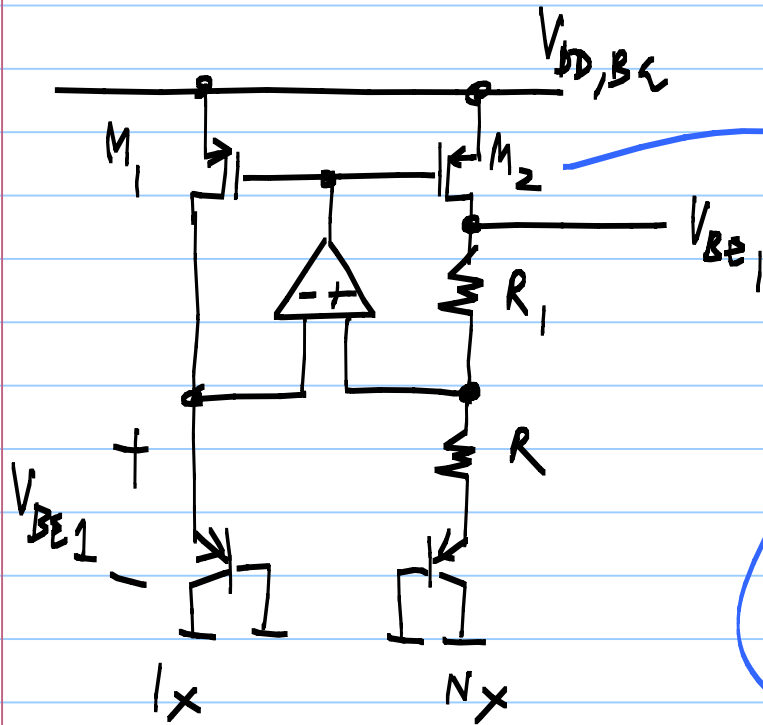
$$\text{Currents in } M_{1,2} : \frac{V_T \ln(N)}{R} + \frac{V_{BE1}}{R_2} = \frac{V_{BE2} \left[V_{BE1} + V_T \ln(N) \frac{R_2}{R} \right]}{R_2}$$

$$V_{DD, min} = V_{BE1} + V_{DSATM1} \approx \underline{\underline{0.9V}}$$



$$I_{D3} = \frac{V_{BE1} + V_T \ln(N) \cdot \frac{R_2}{R}}{R_2}; \quad V_{out} = \left[V_{BE1} + V_T \ln(N) \frac{R_2}{R} \right] \left(\frac{R_3}{R_2} \right)$$

Fractional bandgap reference
(E_{g0}/q)



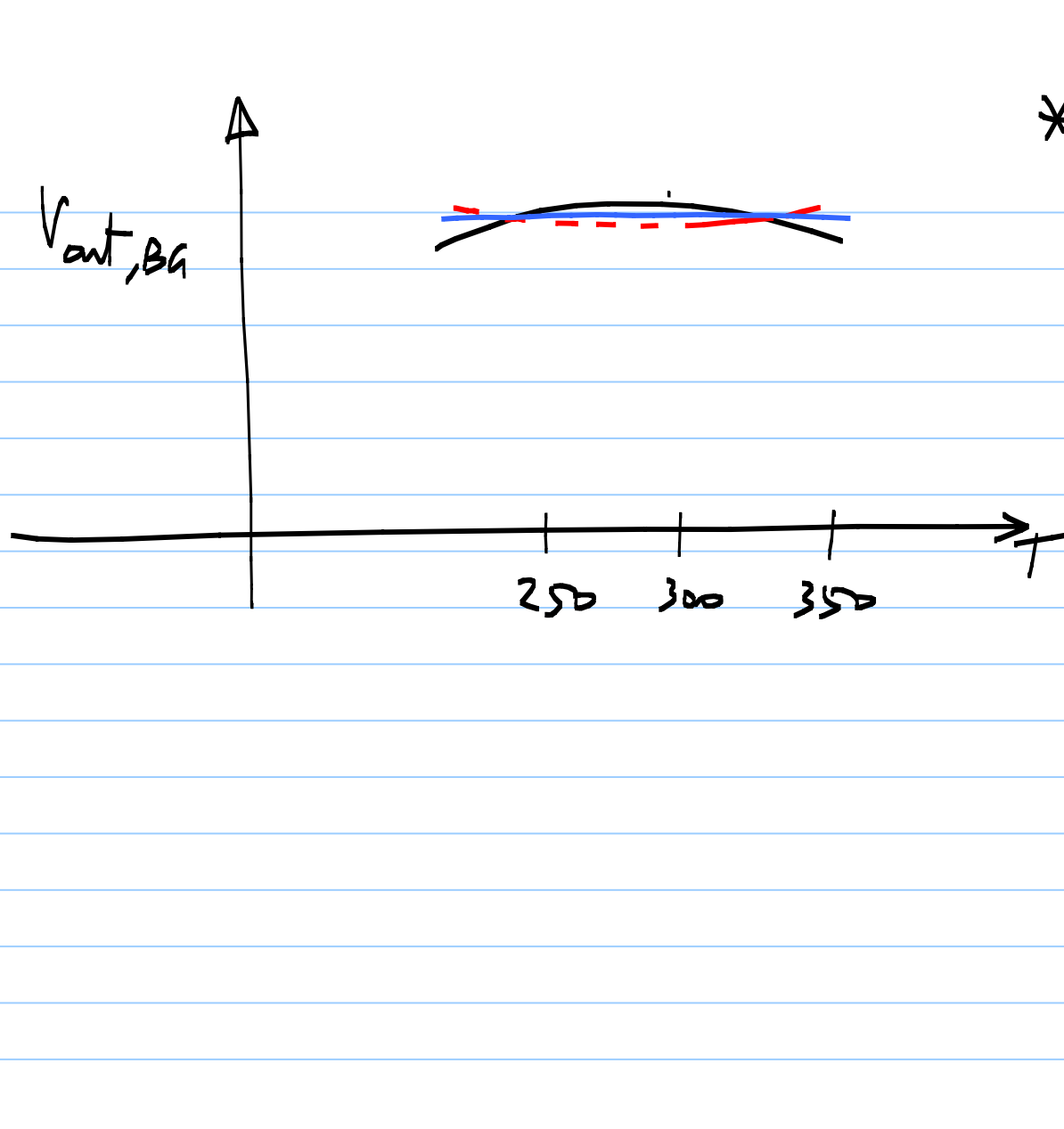
$M_{1,2}$: different V_{SD}

different

* Cascode current mirror

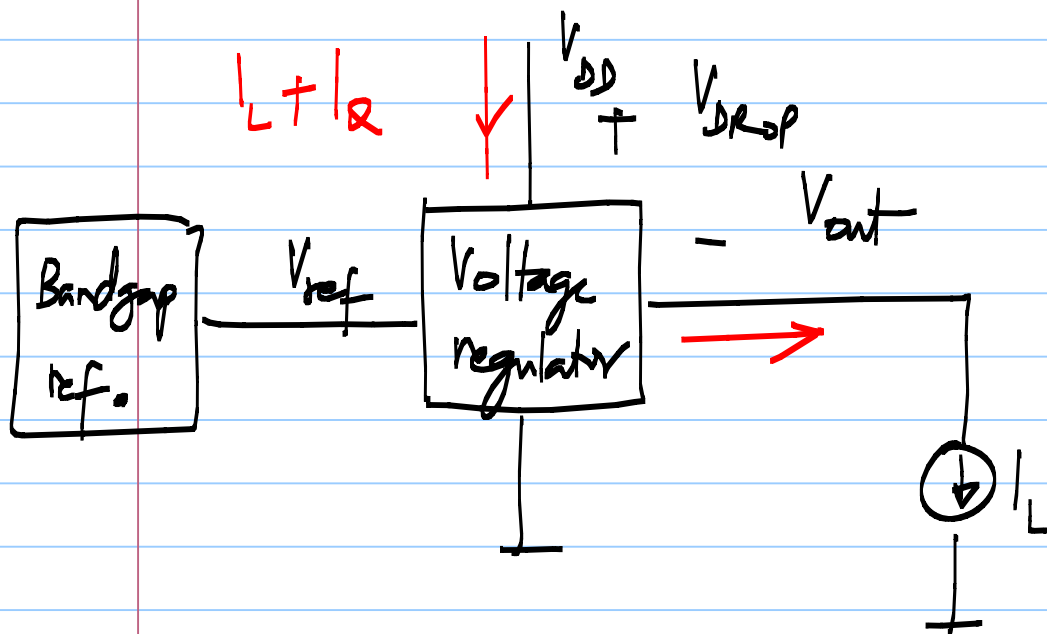
* $V_{DD,BQ}$: regulated

Reducing PS sensitivity



* For reducing variations with temperature even further, use curvature compensation

Voltage regulators:



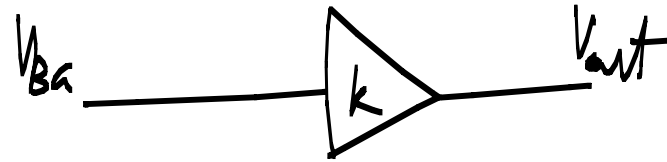
$$P_{out} = V_{out} \cdot I_L$$

$$P_{in} = V_{DD} \cdot (I_L + I_Q)$$

$$\eta = \frac{P_{out}}{P_{in}} = \frac{V_{out}}{V_{DD}} \cdot \frac{I_L}{I_L + I_Q}$$

$$= \frac{V_{out}}{V_{out} + V_{drop}} \cdot \frac{I_L}{I_L + I_Q}$$

Voltage regulator:



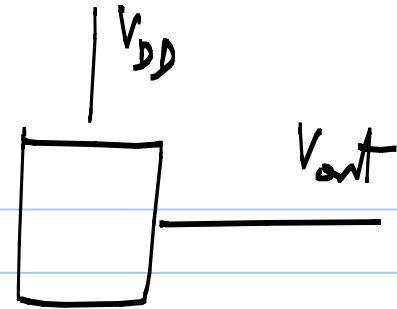
* Feedback controlled voltage source with a low R_{out}

* Scaled version of the bandgap voltage

* "swing limit" : must be close to V_{DD}

* ^{only} $I_L > 0$ need be supported

* Low dropout



* $\frac{\Delta V_{out}}{\Delta I_L} = R_{out}$ must be small (load regulation)

* $\frac{\Delta V_{out}}{\Delta V_{DD}}$: must be small (line regulation)

* Output voltage must be accurately defined

* Transient I_L (step) : ΔV_{out} must be small

$$* \left| \frac{V_{out}(f)}{V_{DD}(f)} \right|$$

Transfer function (small signal)
from V_{DD} to V_{out}

$$\ll 1$$

$$\frac{1}{PSRR}$$

Power supply rejection
ratio

Low dropout:

Poles inside

1.2V

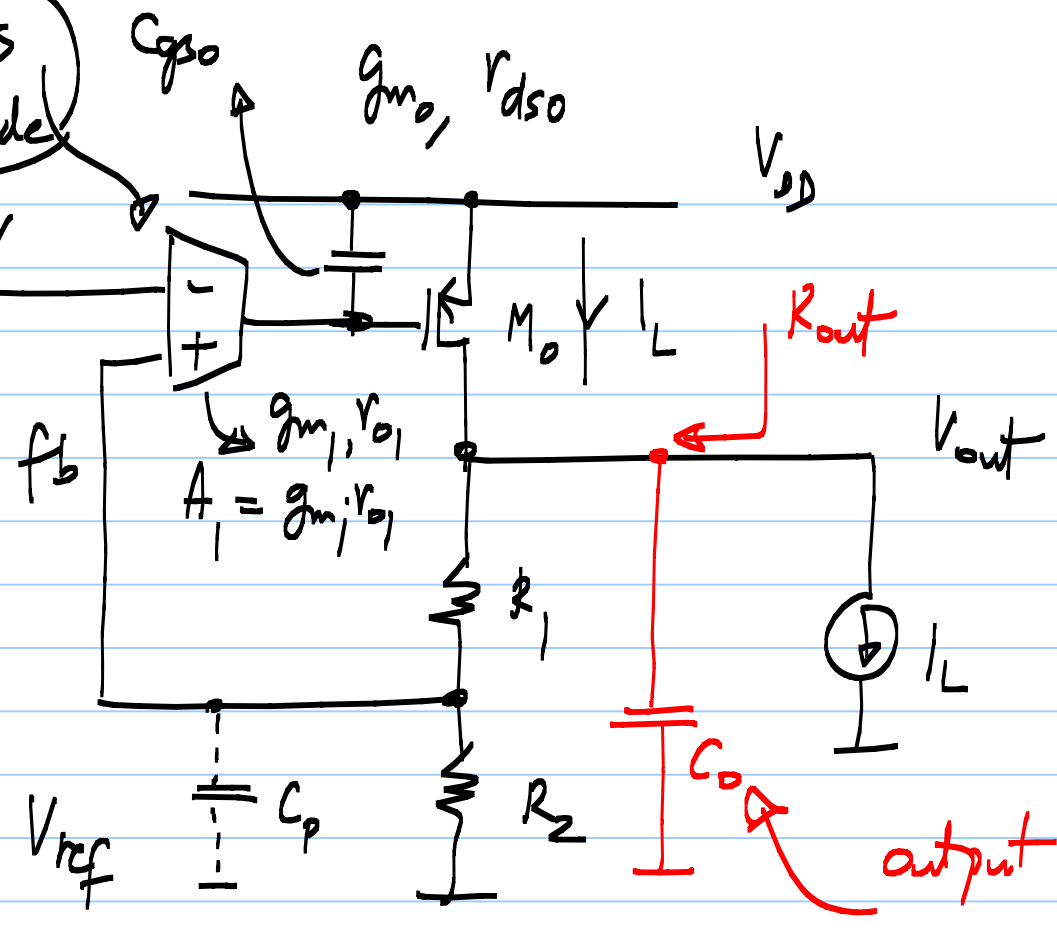
$$V_{out} = 2.4V$$

$$V_{ref} = 1.2V$$

(Bk)

In steady state,

$$V_{out} = \frac{R_1 + R_2}{R_2} \cdot V_{ref}$$



A_0 : dc loop gain:

$$R_{out} = \frac{r_{ds0}}{1 + A_0}$$

output capacitor

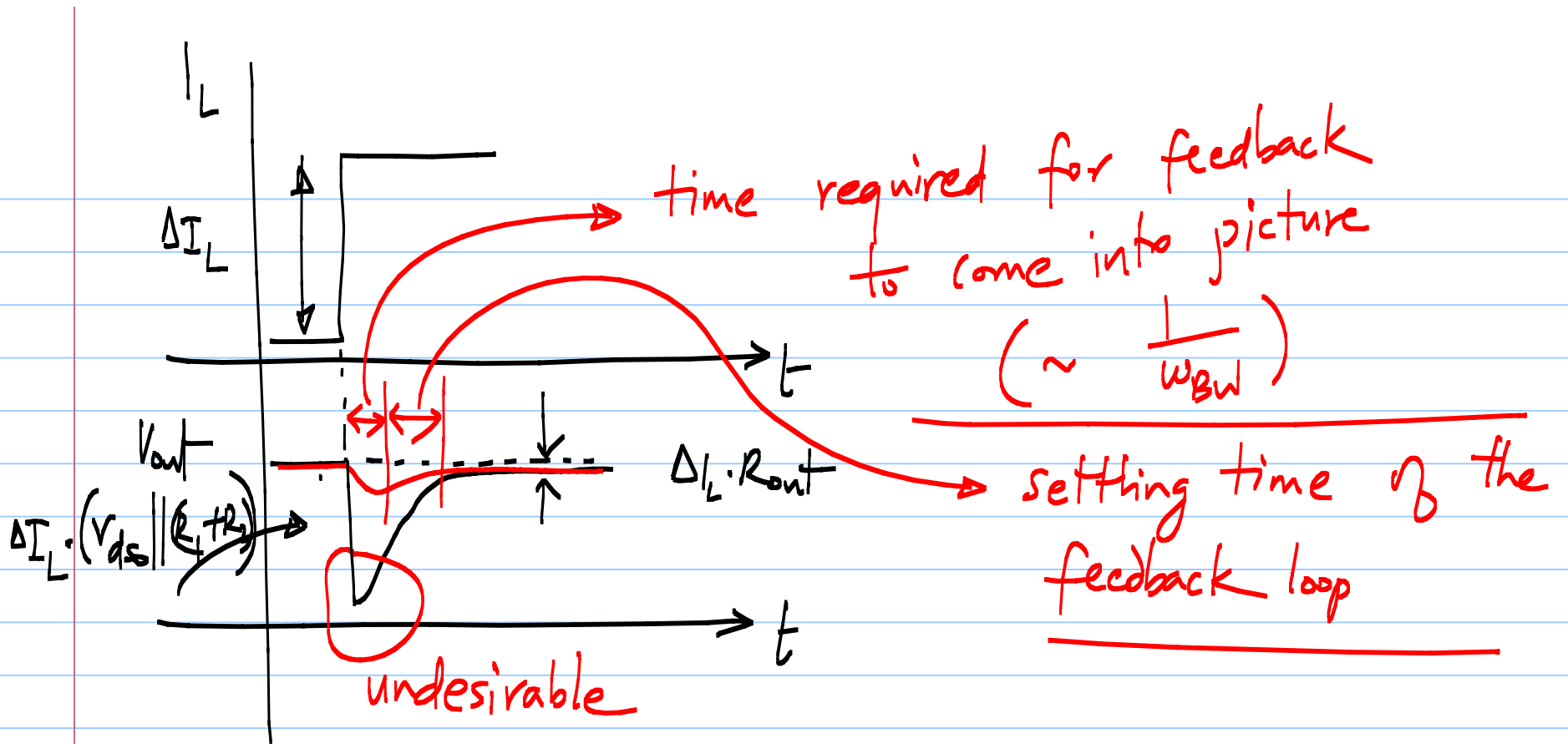
gain, transconductor: $g_{m1} \cdot r_{o1}$

$$M_o : g_{m_o} [r_{ds_o} \parallel (R_1 + R_2)] \approx g_{m_o} \cdot r_{ds_o}$$

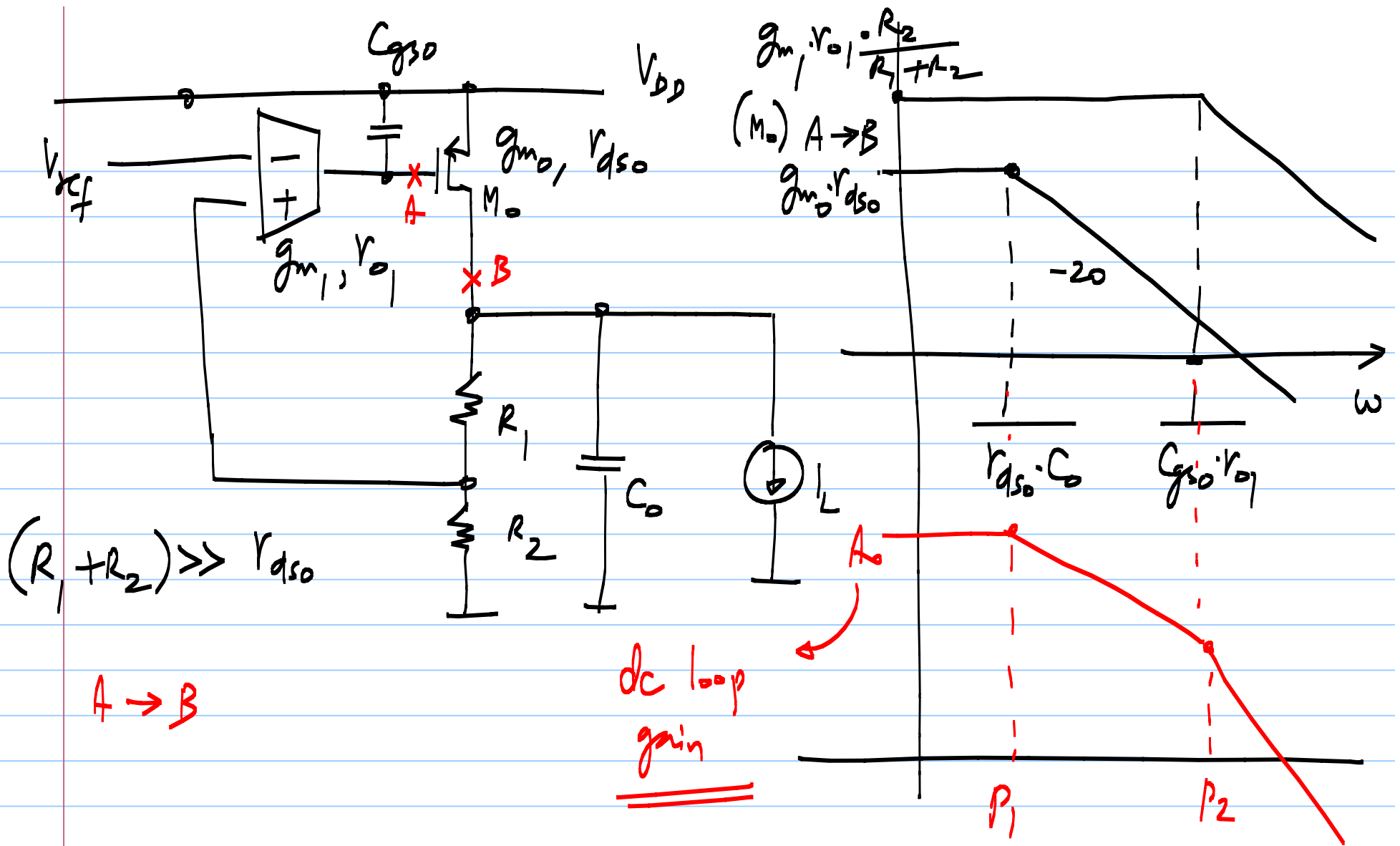
$$A_o = \underline{g_{m1} \cdot r_{o1} \cdot g_{m_o} \cdot r_{ds_o} \left(\frac{R_2}{R_1 + R_2} \right)}$$

R_{out} small if A_1 is large

I_L step from low to high values \rightarrow large change in the output.



- Poles due to
- (i) C_o
 - (ii) C_{gs0}
 - X (iii) Poles in g_m , i feedback divider



$$p_2 > \omega_{n/loop}$$

$$\left[\frac{1}{C_{gs0} \cdot r_{o1}} \right] > g_{m1} r_{o1} \cdot g_{m2} r_{ds0} \left(\frac{R_2}{R_1 + R_2} \right) \cdot \frac{1}{r_{ds0} C_{o1}}$$

A_0

