Question Bank

a. Overview of 8051 micro controller architecture

Q1. What are the advantages and disadvantages of using Harvard architecture in 8051?

Q2. How much maximum external program memory can be interfaced?

Q3. Explain PSW SFR. Give the application differences between Carry and Overflow flags

Q4. What are the power consumptions in power down and idle modes

Q5. Explain Quasi Bidirectional ports of 8051

Q6. What is the status of all registers on reset?

Q7. What is the maximum delay the Timer0 produces when 8051 is operated at 12MHz?

Q8. Explain how in Serial communication mode 0 expands I/O lines with the help of shift

b. Overview of 8096 micro controller architecture

Q1. How many bytes are there in the internal memory?

Q2. Explain all the bits in the PSW

Q3. How much program memory is in the chip and how much more can be interfaced externally?

Q4. List and explain all SFRs

Q5. How DAC is realized using PWM output of 8096

Q6. What is the maximum delay the Timer0 produces when 8096 is operated at 12MHz?