

1. The 8051 microcontroller is of ___pin package as a _____ processor.
a) 30, 1byte b) 20, 1 byte c) 40, 8 bit d) 40, 8 byte
2. The SP is of ___ wide register. And this may be defined anywhere in the _____.
a) 8 byte, on-chip 128 byte RAM. b) 8 bit, on chip 256 byte RAM.
c) 16 bit, on-chip 128 byte ROM d) 8 bit, on chip 128 byte RAM.
3. After reset, SP register is initialized to address_____.
a) 8H b) 9H c) 7H d) 6H
4. What is the address range of SFR Register bank?
a) 00H-77H b) 40H-80H c) 80H-7FH d) 80H-FFH
5. Which pin of port 3 is has an alternative function as write control signal for external data memory?
a) P3.8 b) P3.3 c) P3.6 d) P3.1
6. What is the Address (SFR) for TCON, SCON, SBUF, PCON and PSW respectively?
a) 88H, 98H, 99H, 87H, 0D0H. b) 98H, 99H, 87H, 88H, 0D0H
c) 0D0H, 87H, 88H, 99H, 98H d) 87H, 88H, 0D0H, 98H, 99H
7. Match the following:
1) TCON i) contains status information
2) SBUF ii) timer / counter control register.
3) TMOD iii) idle bit, power down bit
4) PSW iv) serial data buffer for Tx and Rx.
5) PCON v) timer/ counter modes of operation.
a) 1->ii, 2->iv, 3->v, 4->i, 5->iii. b) 1->i, 2->v, 3->iv, 4->iii, 5->ii.
c) 1->v, 2->iii, 3->ii, 4->iv, 5->i. d) 1->iii, 2->ii, 3->i, 4->v, 5->iv.
8. Which of the following is of bit operations?
i) SP
ii) P2
iii) TMOD
iv) SBUF
v) IP
a) ii, v only b) ii, iv, v only c) i, v only d) iii, ii only
9. Serial port interrupt is generated, if ___ bits are set
a) IE b) RI, IE c) IP, TI d) RI, TI
10. In 8051 which interrupt has highest priority?
a) IE1 b) TF0 c) IE0 d) TF1
11. Intel 8096 is of ___ bit microcontroller family called as _____.
a) 8, MCS51 b) 16, MCS51 c) 8, MCS96 d) 16, MCS96
12. 8096 has following features fill up the following,
i) ___ Register file,
ii) ___ I/O Ports
iii) ___ architecture.
a) 256 byte, five 8bit, register to register
b) 256 byte, four 8bit, register to register
c) 232 byte, five 8bit, register to register
d) 232 byte, six 8 bit, register to register
13. How many synchronous and asynchronous modes are there in serial port of 8096?

- a) 2, 2 respectively b) 3,1 respectively c) 1, 3 respectively d) 1, 2 respectively
14. In 8096 we have ____ interrupt sources and _____ interrupt vectors.
a) 18, 8 b) 21, 6 c) 21, 8 d) 16, 8
15. 8096 has ___ general purpose I/O ports, Port 2 includes _____ of the following
i) two quasi-bidirectional I/O lines
ii) two output lines
iii) four input lines
iv) open drain outputs
a) 4, i, iv b) 6, ii, iii c) 4, i,ii,iii d) 6, i, ii, iv
16. 8096 write-protected mode, no code can write to memory address between ____.
a) 2020 to 3FFFH b) 8000 to FFFFH c) 2000 to 3FFFH d) 2020 to 202FH
17. If the __ pin is __ , then we have the option of using the ____ ROM or EPROM together with _____ memory and devices.
a) EA, high, internal, external
b) EA, low, internal, external
c) EA, high, external, internal
d) EA, low, external, internal
18. In 8096, CCB bit 3 is ____.
a) write strobe mode select b) address valid strobe select
c) bus width select d) Internal read control mode
19. In 8096, mode ____ of serial port are ____ modes commonly used for ____ communications.
a) 1, 8bit, single processor b) 0, 7bit, multiple microcontroller
c) 2, 9 bit, multiple processors d) 3, 8 bit, multiple microcontroller
20. What is the function of watchdog timer?
a) The watchdog Timer is an external timer that resets the system if the software fails to operate properly.
b) The watchdog Timer is an internal timer that sets the system if the software fails to operate properly.
c) The watchdog Timer is an internal timer that resets the system if the software fails to operate properly.
d) None of them

Key:

5.1 C 5.2 D 5.3 C 5.4 D 5.5 C 5.6 A
5.7 A 5.8 A 5.9 D 5.10 C 5.11 D 5.12 C
5.13 C 5.14 C 5.15 C 5.16 C 5.17 A 5.18 B
5.19 C 5.20 C