1. The coprocessors operate in _____ with a processor on the same buses and with the same instruction _____.
   a) Parallel, byte stream.   b) Series, byte stream.
   c) Series, bite stream   d) Parallel, bite stream.

2. Why 8087 is referred to as Coprocessor?
   i) Because 8087 is used in parallel with main processor in a system, rather than serving as a main processor itself.
   ii) Because 8087 is used in serial with main processor in a system, rather than serving as a main processor itself.
   iii) Because main Microprocessor handles the general program execution and the 8087 handles specialized math computations.
   a) i & iii   b) ii & iii   c) iii only.   d) i only.

3. 8087 connection to 8086, to enable the _____ bank of memory _____pins are to be connected.
   a) Lower, BHE   b) Upper, BHE   c) Lower, INT
   d) Upper, INT.

4. ___ Connection and the _____ instruction will solve the problem of synchronization between processor and coprocessor.
   a) INT & NMI, WAIT   b) RQ/GT_0 & RQ/GT_1, FWAIT
   c) BUSY & TEST, FWAIT   d) S_0 & QS_0, WAIT

5. _____ input is available, so that another coprocessor can be connected and function in _____ with the 8087.   .
   a) RQ/GT_0, parallel   b) RQ/GT_1, parallel   c) QS_1 & QS_0, parallel
   d) S_0 & S_1, parallel.

6. In 8087, _____ many register stack are there? And of _____ registers. These registers are used as _____ stack.
   a) 7, 40 bit, FIFO. b) 8, 60 bit, LILO. c) 8, 80 bit, LIFO d) 7, 80 bit, FILO.

7. If _____ and _____ connections are made so that an error condition in 8087 can interrupt to the processor.
   a) BHE, RQ/GT_1 b) BUSY, TEST   c) INT, NMI   d) RQ/GT_0, RQ/GT_1

8. In 8087, which instruction is used for division real reversed_____.
   a) FDIV   b) FIDIVR   c) FDIVR   d) FDIVRP

9. Which of the following is of compare instruction in 8087?
   a) FTST   b) FPREM   c) FPATAN   d) FLDI

10. In 8087 coprocessor one of the following instructions is not valid
    a. FSIN
    b. FPTAN
    c. FIDIV
    d. FSQRT

11. One of the following signals belongs to the 8087 coprocessor is
    a. HOLD
    b. BUSY
    c. TEST
    d. NMI

Key:
4.1 A   4.2 A   4.3 B   4.4 A   4.5 B   4.6 C
4.7 C   4.8 C   4.9 A   4.10 A 4.11 C