

NPTEL Phase-II
Video course on

**Design Verification and Test of
Digital VLSI Designs**

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Module VI: Binary Decision Diagram

Lecture I: Binary Decision Diagram:
Introduction and construction

- Model checking algorithm
 - Polynomial in the size of state machine and the length of the formula
- Problem with model checking
 - State space explosion problem

Binary Decision Diagrams (BDD)

- Based on recursive Shannon expansion

$$f = x f_x + x' f_{x'}$$

- Compact data structure for Boolean logic
 - can represents sets of objects (states) encoded as Boolean functions
- Canonical representation
 - reduced ordered BDDs (ROBDD) are canonical

Shannon Expansion

$$f = x f_x + x' f_{x'}$$

$$f = ac + bc$$

$$f_{a'} = f(a=0) = bc$$

$$f_a = f(a=1) = c + bc$$

$$\begin{aligned} f &= a f_a + a' f_{a'} \\ &= a(c+bc) + a'(bc) \end{aligned}$$

Binary Decision Tree (BDT)

- Binary Decision Trees are trees whose non-terminal nodes are labeled with Boolean variables x, y, z, \dots and whose terminal nodes are labeled with either 0 or 1.

Binary Decision Tree (BDT)

- Each non-terminal node has two edges, one dashed line and one solid line.
- Dashed line represents 0 and solid line represents 1.

Binary Decision Tree

a	b	c	f
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	0
1	1	1	1

$$f = ac + bc$$

Truth table

Truth Table \rightarrow BDT

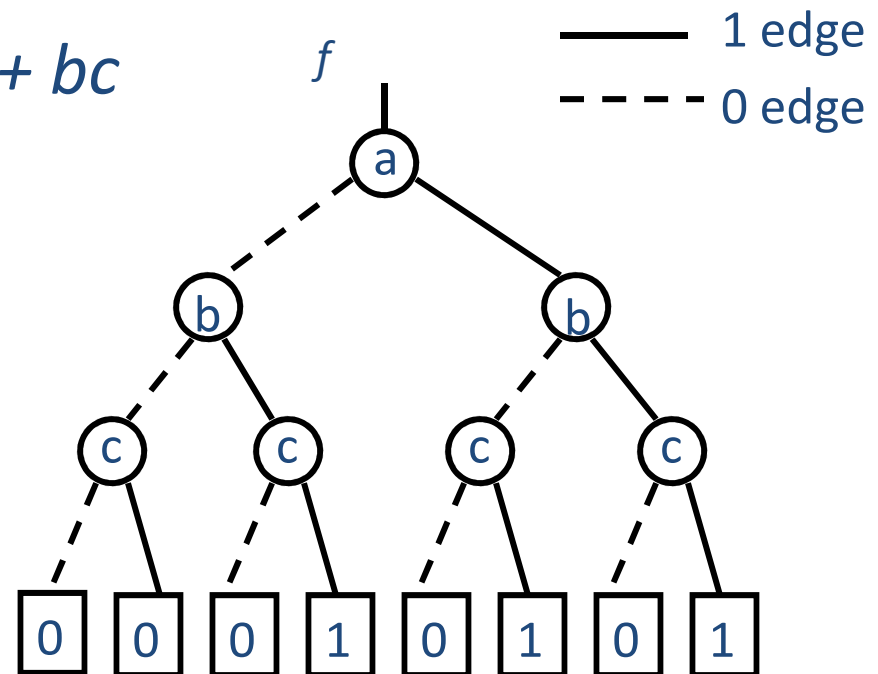
Binary Decision Tree

Truth Table \rightarrow BDT

a	b	c	f
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	0
1	1	1	1

Truth table

$$f = ac + bc$$

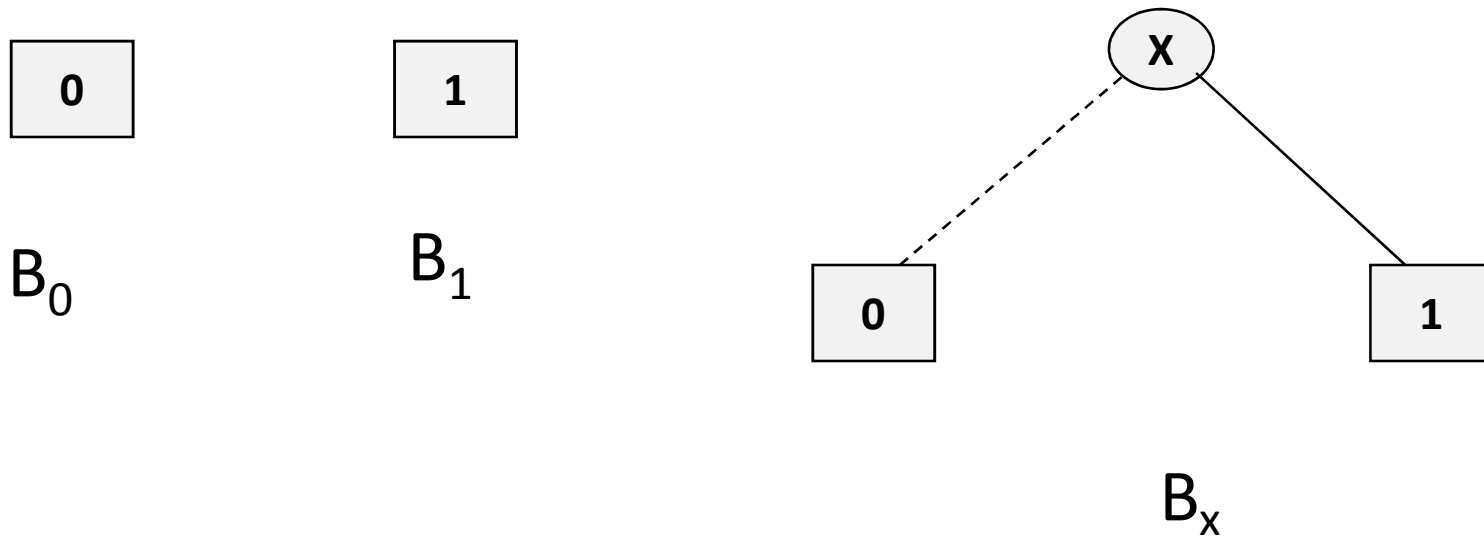


Decision tree

Binary Decision Diagram

- A Binary Decision Diagram (BDD) is a finite DAG with an unique initial node, where
 - all terminal nodes are labeled with 0 or 1
 - all non-terminal nodes are labeled with a Boolean Variable.
 - Each non-terminal node has exactly two edges from that node to others; one labeled 0 and one labeled 1; represent them as a dashed line and a solid line respectively

Binary Decision Diagram



B_0 representing the Boolean constant 0

B_1 representing the Boolean constant 1

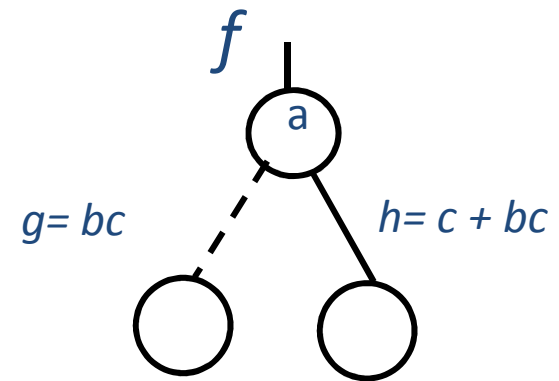
B_x representing the Boolean variable x

Shannon Expansion \rightarrow BDD

$$f = ac + bc$$

$$f = x f_x + x' f_{x'}$$

- $f_{a'} = f(a=0) = bc = g$
- $f_a = f(a=1) = c + bc = h$

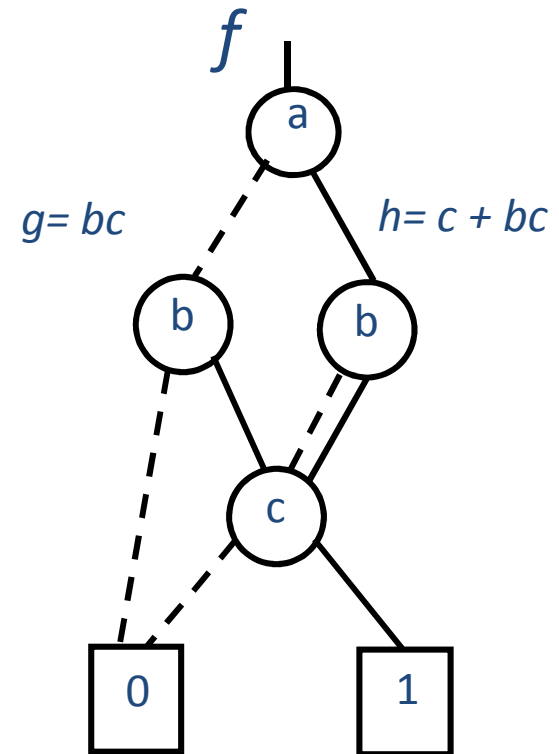


Shannon Expansion \rightarrow BDD

$$f = ac + bc$$

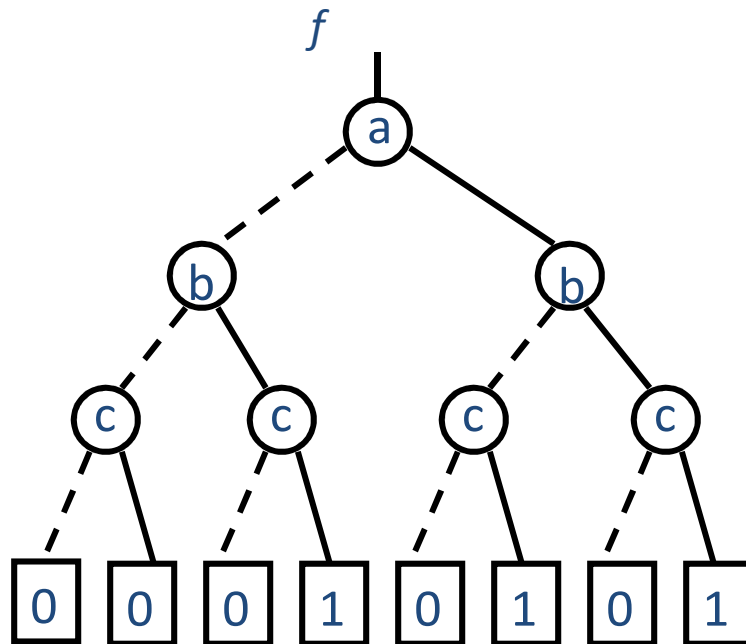
$$f = x f_x + x' f_{x'}$$

- $f_{a'} = f(a=0) = bc = g$
- $f_a = f(a=1) = c + bc = h$
- $g_{b'} = (bc)_{|b=0} = 0$
- $g_b = (bc)_{|b=1} = c$
- $h_{b'} = (c+bc)_{|b=0} = c$
- $h_b = (c+bc)_{|b=1} = c$

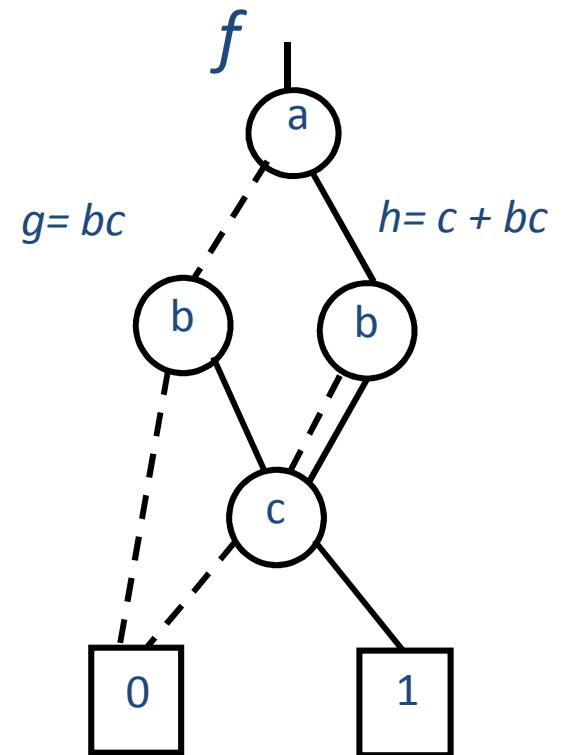


Binary Decision Tree and Diagram

$$f = ac + bc$$



From Truth Table



From Shannon Expression

BDD Reduction Rules -1

Eliminate *duplicate terminals*

If a BDD contains more than one terminal 0-node, then we redirect all edges which point to such a 0-node to just one of them.

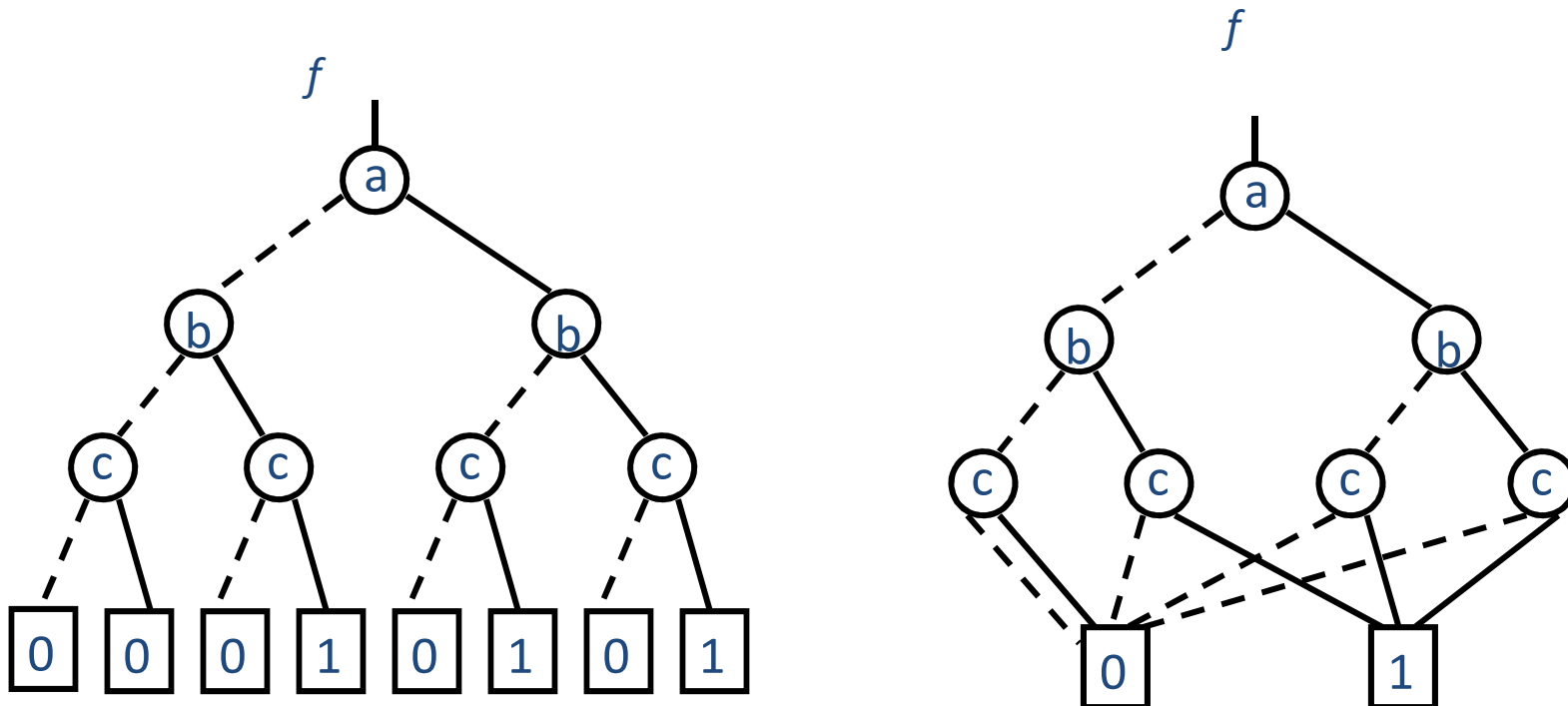
Similarly, we proceed for nodes labeled with 1.

BDD Reduction Rules -1

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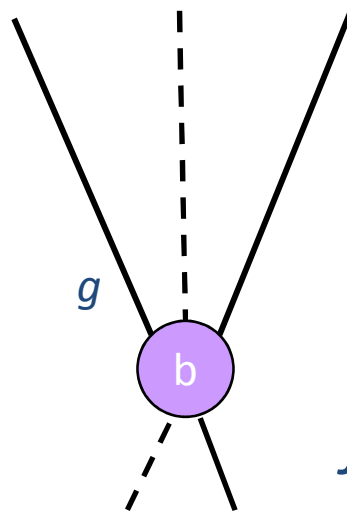
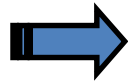
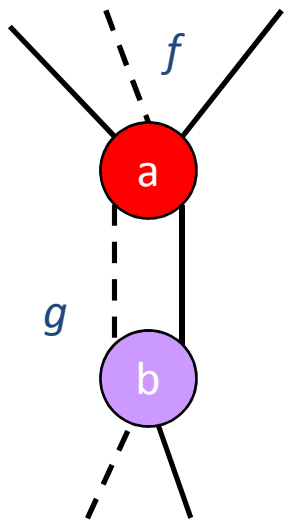
Similarly, we proceed for nodes labeled with 1.



BDD Reduction Rules -2

Eliminate *redundant* nodes

(with both edges pointing to same node)

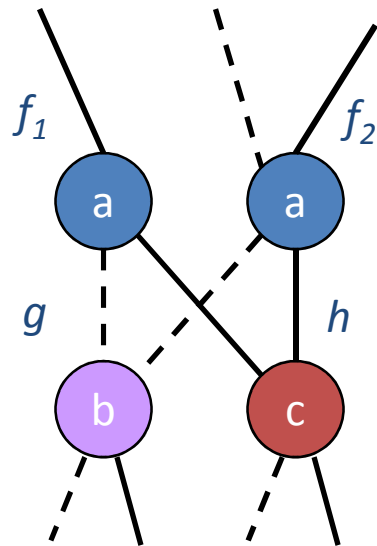


$$f = a' g(b) + a g(b) = g(b)$$
$$(f_a + f_{a'} = 1)$$

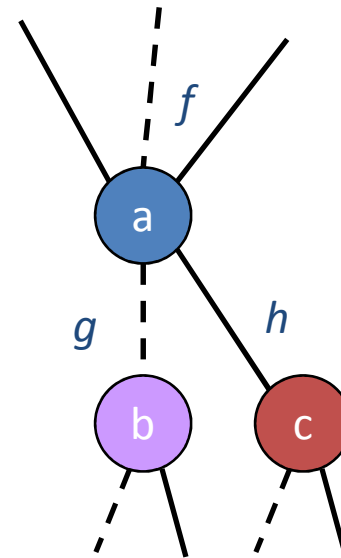
BDD Reduction Rules -3

Merge duplicate nodes

- Nodes must be unique



$$f_1 = a' g(b) + a h(c) = f_2$$



$$f = f_1 = f_2$$

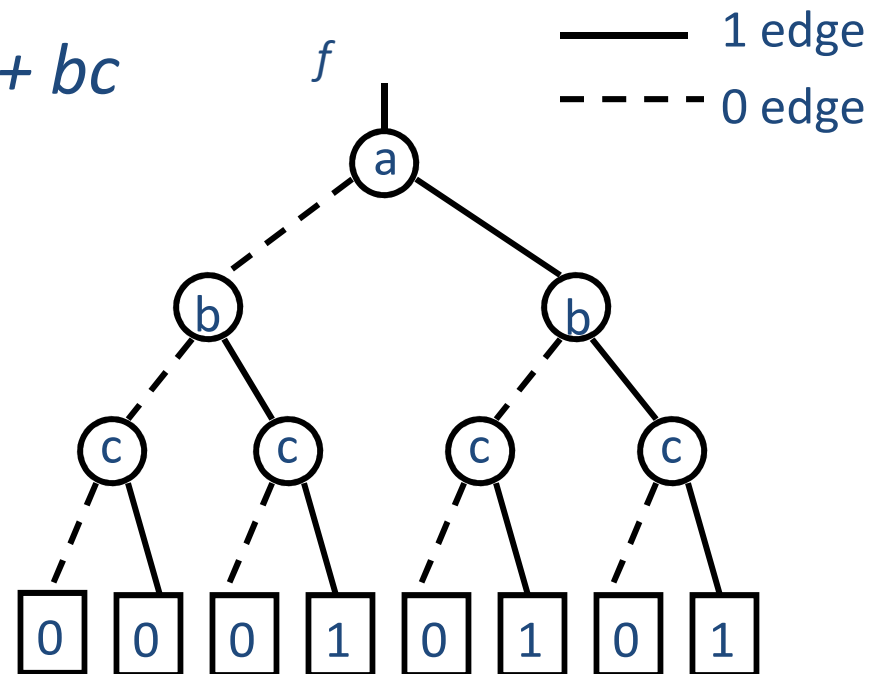
BDD Construction

- Reduced BDD

a	b	c	f
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	0
1	1	1	1

Truth table

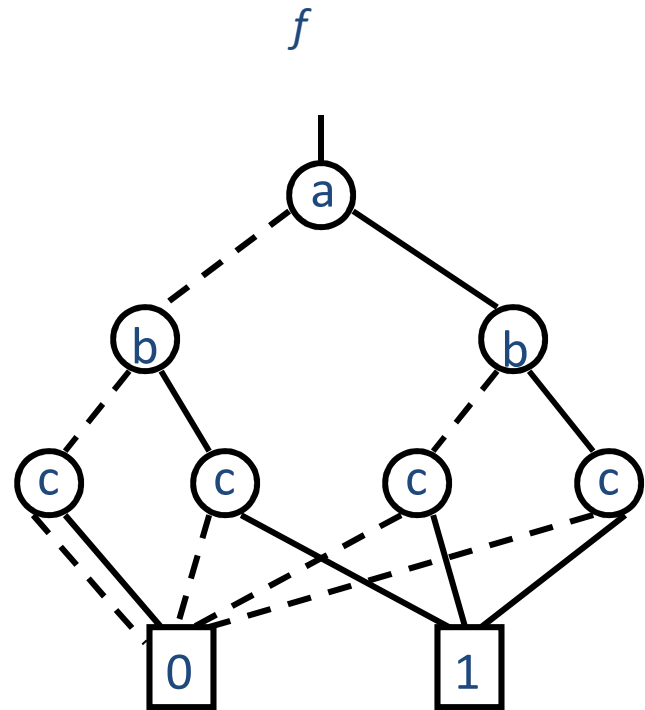
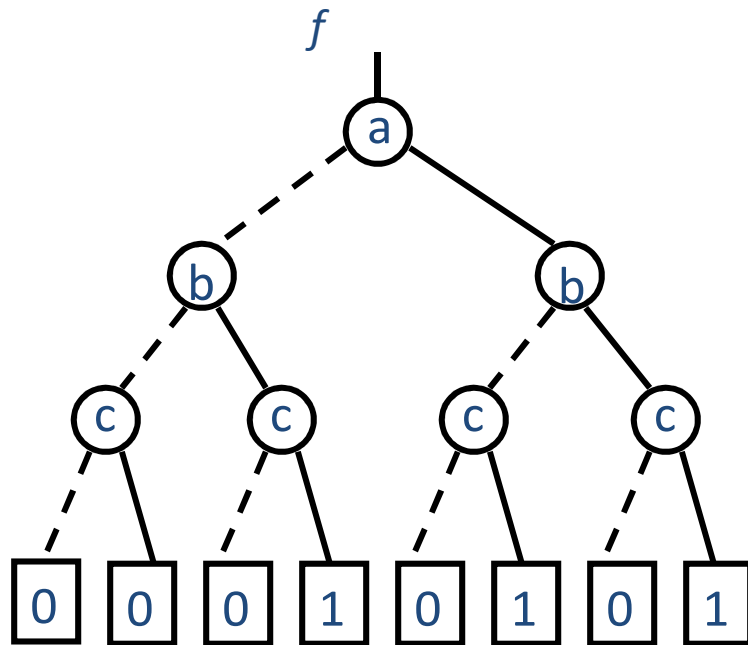
$$f = ac + bc$$



Decision tree

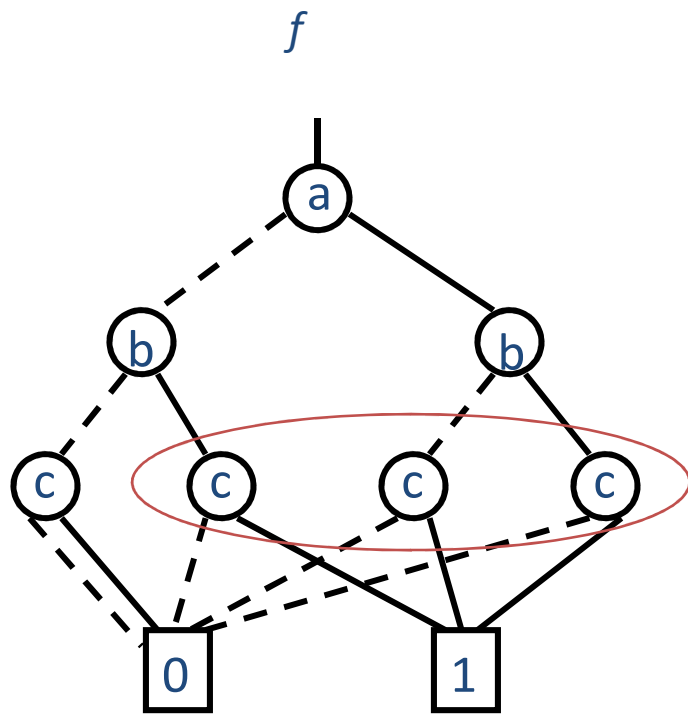
BDD Reduction

$$f = ac + bc$$

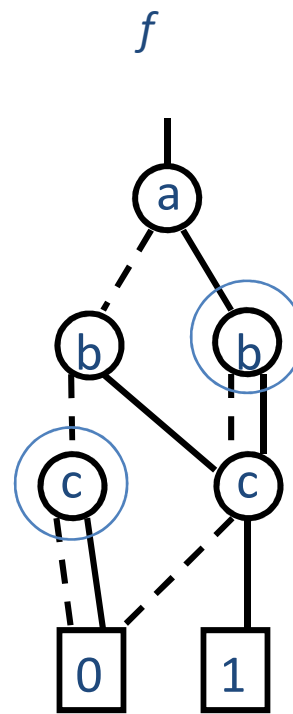


1. Merge terminal nodes

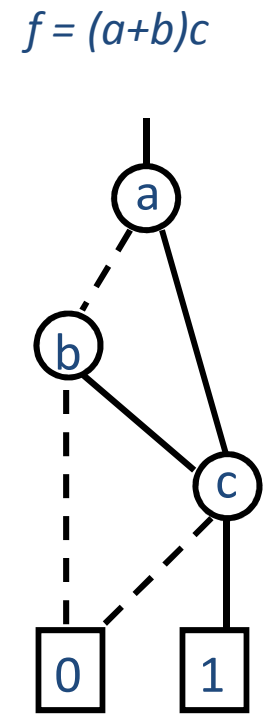
BDD Construction – cont'd



2. Merge duplicate nodes



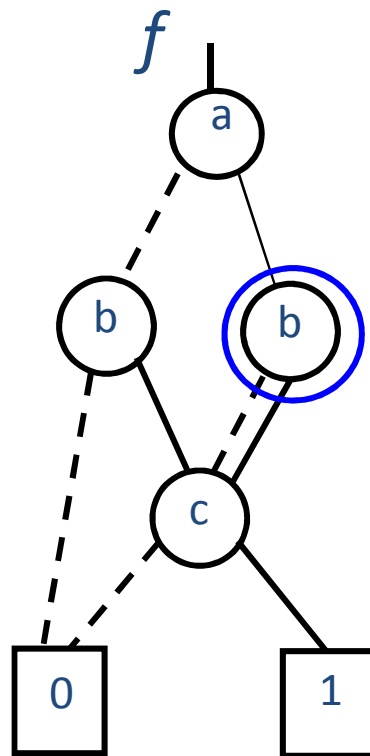
3. Remove redundant nodes



Reduced BDD

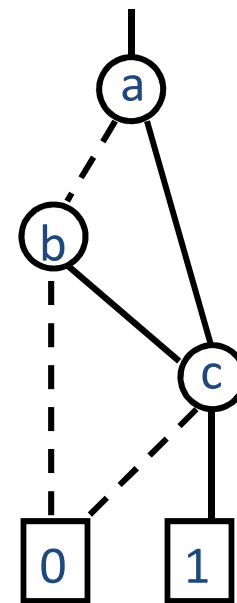
BDD Construction – cont'd

BDD constructed by Shannon Expression



3. Remove redundant nodes

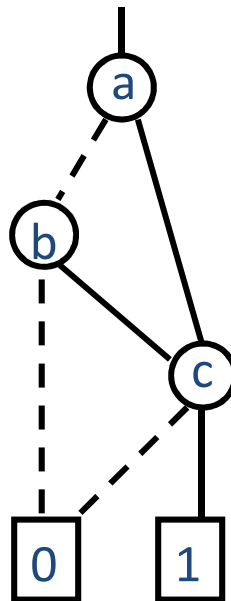
$$f = (a+b)c$$

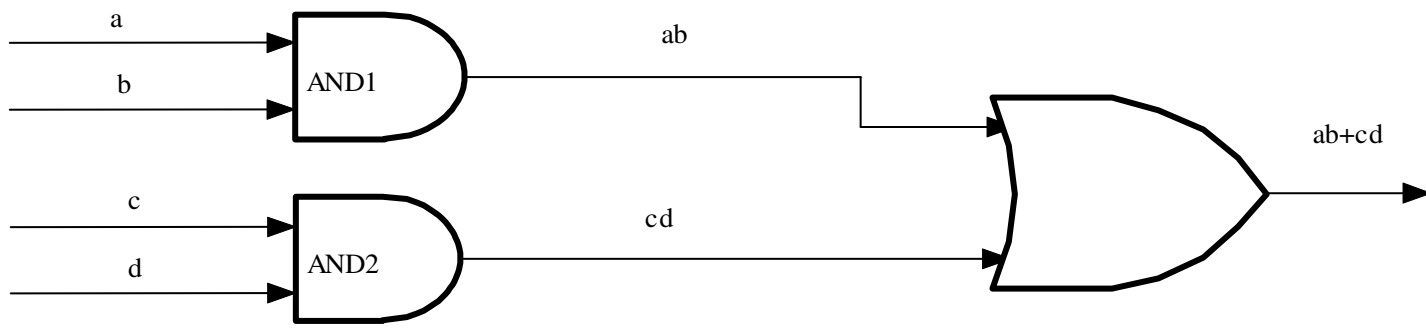


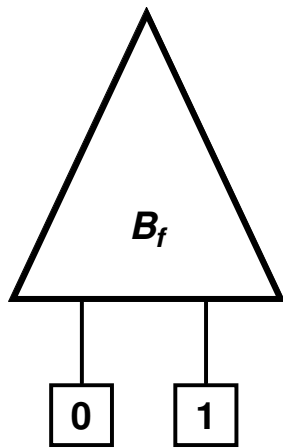
Reduced BDD

Reduced BDDs

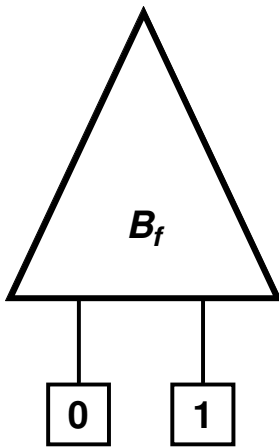
A BDD is said to be reduced if none of the reduction rules R1-R3 can be applied (i.e., no more reductions are possible)



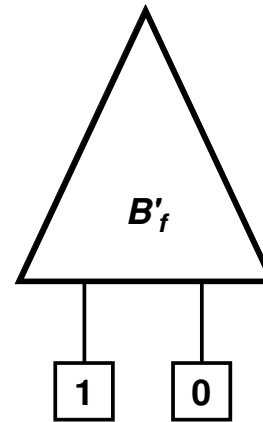




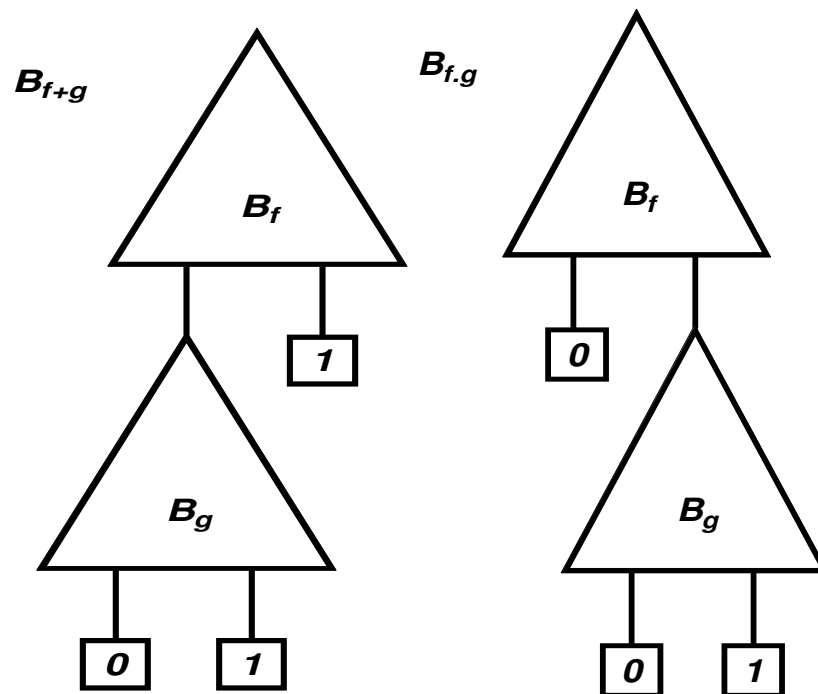
BDD B_f for Boolean function f



BDD B_f for Boolean function f



BDD B'_f for Boolean function f'



BDDs for $f+g$ and $f.g$

Questions

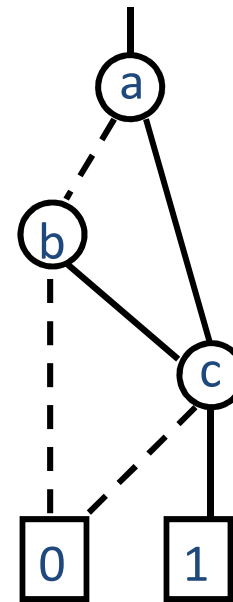
1. Do we get any advantage in using BDT.
2. While constructing the BDD, is it required to start from BDT.
3. The definition of BDD does not restrict the occurrence of a variable in any number of times in a path. Show that it may lead to inconsistency with an example.
4. Is reduced BDD of any function is unique.

Shannon Expansion \rightarrow BDD

$$f = ac + bc$$

$$f = x f_x + x' f_{x'}$$

- $f_{a'} = f(a=0) = bc = g$
- $f_a = f(a=1) = c + bc = h$
- $g_{b'} = (bc)_{|b=0} = 0$
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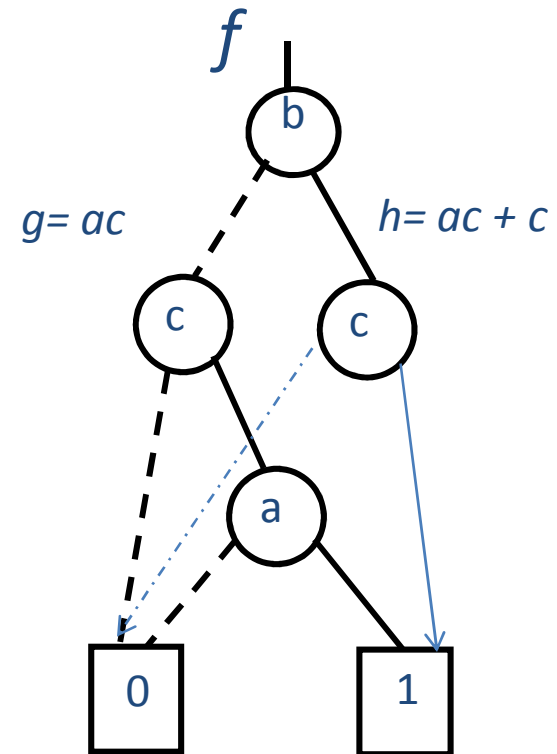


Shannon Expansion \rightarrow BDD

$$f = ac + bc$$

$$f = x f_x + x' f_{x'}$$

- $f_{b'} = f(b=0) = ac = g$
- $f_b = f(b=1) = ac + c = h$
- $g_{c'} = (ac)_{|c=0} = 0$
- $g_c = (ac)_{|c=1} = a$
- $h_{c'} = (ac+c)_{|c=0} = 0$
- $h_c = (ac+c)_{|c=1} = 1$



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Module VI: Binary Decision Diagram

Lecture II: : Ordered Binary Decision Diagram

Binary Decision Diagram

- Construction of BDD
- Reduced BDD

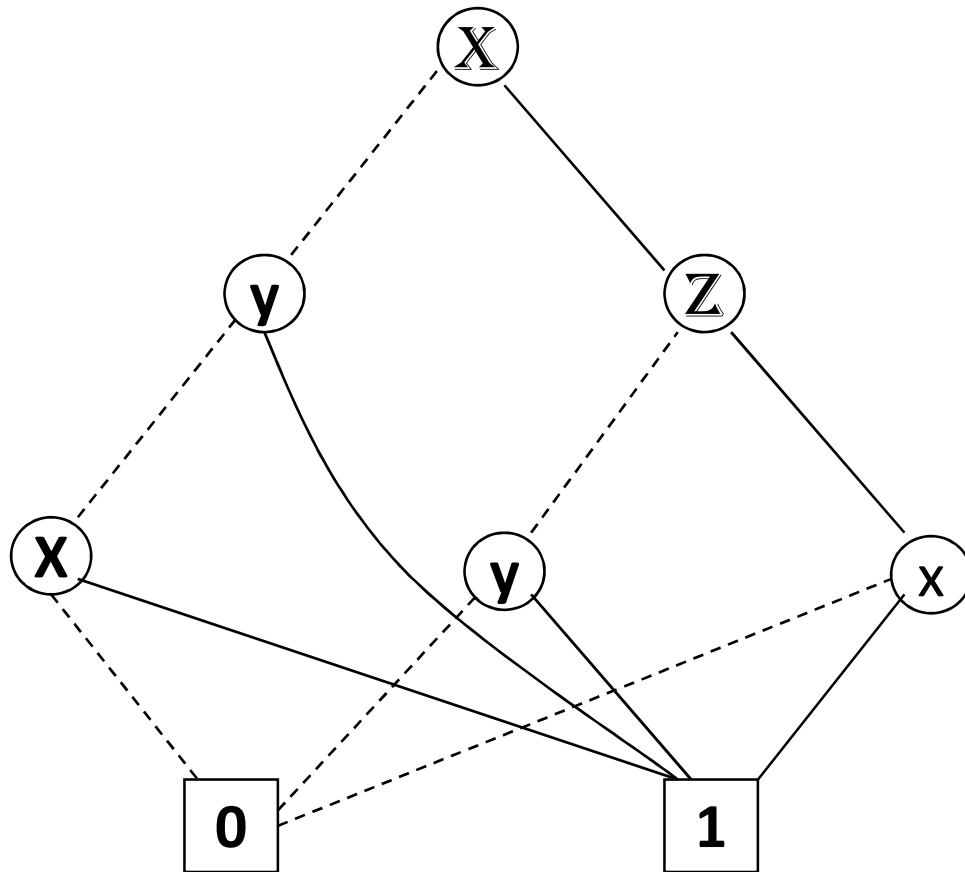
Binary Decision Diagram

- Occurrence of variables
- Ordering of variables

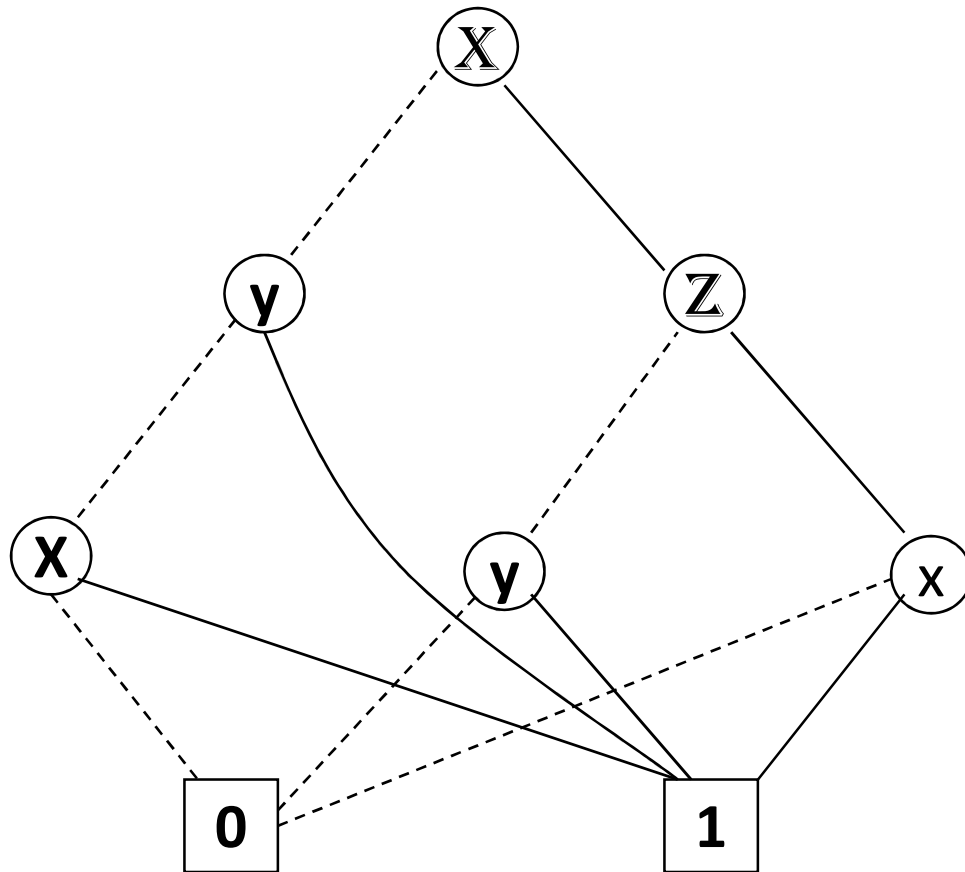
Binary Decision Diagram

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 - all terminal nodes are labeled with 0 or 1
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Ordering of Variables



Ordering of Variables

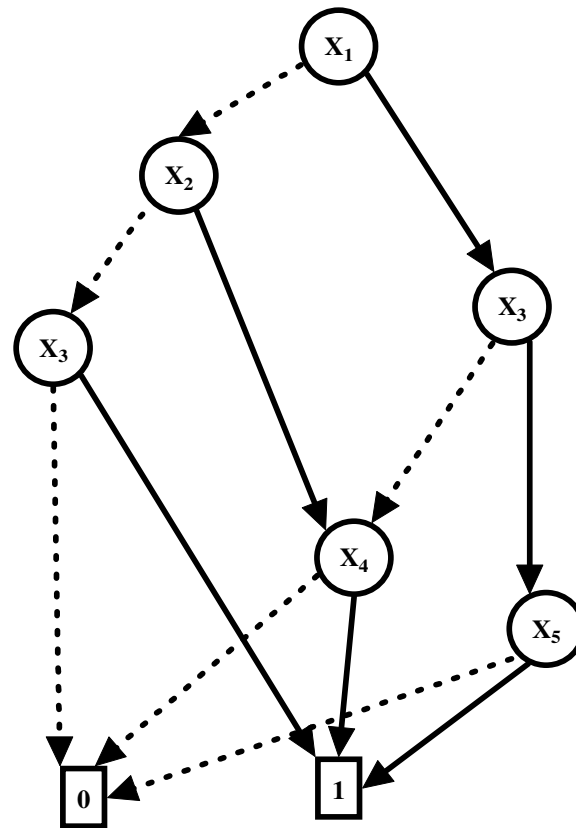


- Evaluation Path
 - Consistent
 - Inconsistent

Ordered BDDs (OBDDs)

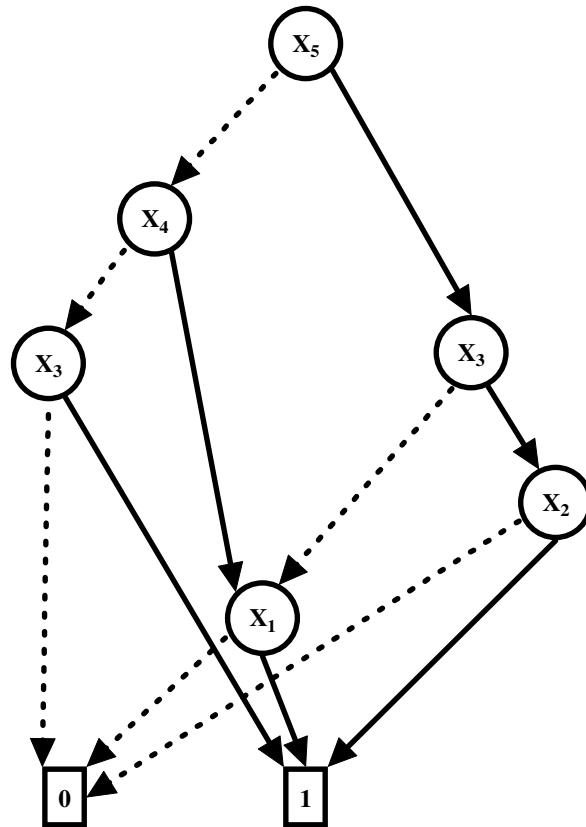
- Let $[x_1, x_2, \dots, x_n]$ be an ordered list of variables without duplication and let B be a BDD all of whose variables occur somewhere in the list.
- We say that B has the ordering $[x_1, x_2, \dots, x_n]$ if all variable labels of B occur in that list and, for every occurrence of x_i followed by x_j along any path in B , we have $i < j$.

OBDDs



BDD with variable ordering $[x_1, x_2, x_3, x_4, x_5]$

OBDDs

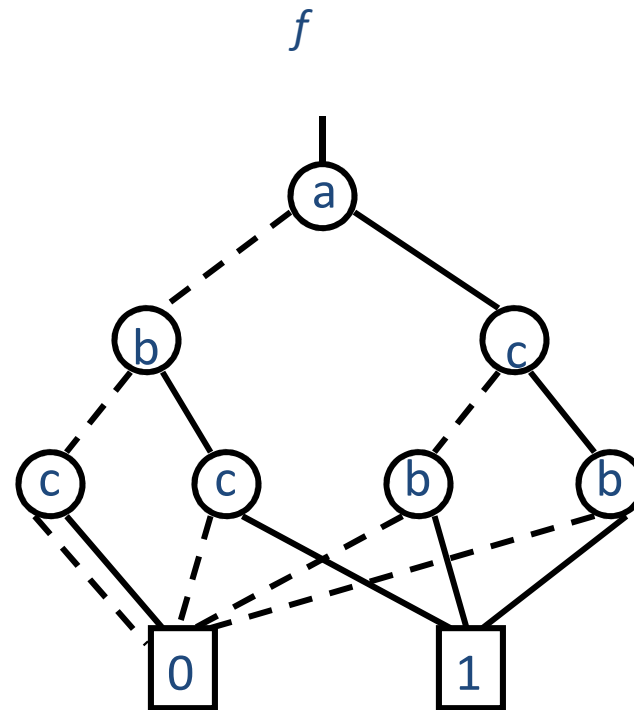


BDD with variable ordering $[x_5, x_4, x_3, x_2, x_1]$

Reduced Ordered BDDs (ROBDDs)

Not a Ordered BDD.

Not a Reduced BDD.



Impact of the chosen variable ordering

- In general the chosen variable ordering makes a significant difference to the size of the OBDD representing a given function.

Impact of the chosen variable ordering

- Consider the Boolean function

$$- f = (x_1 + x_2).(x_3 + x_4).(x_5 + x_6).....(x_{2n-1} + x_{2n})$$

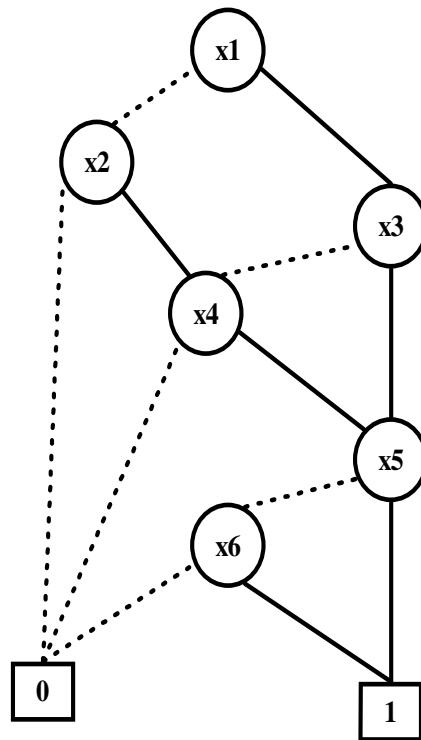
Impact of the chosen variable ordering

- Consider the Boolean function

$$- f = (x_1 + x_2).(x_3 + x_4).(x_5 + x_6).....(x_{2n-1} + x_{2n})$$

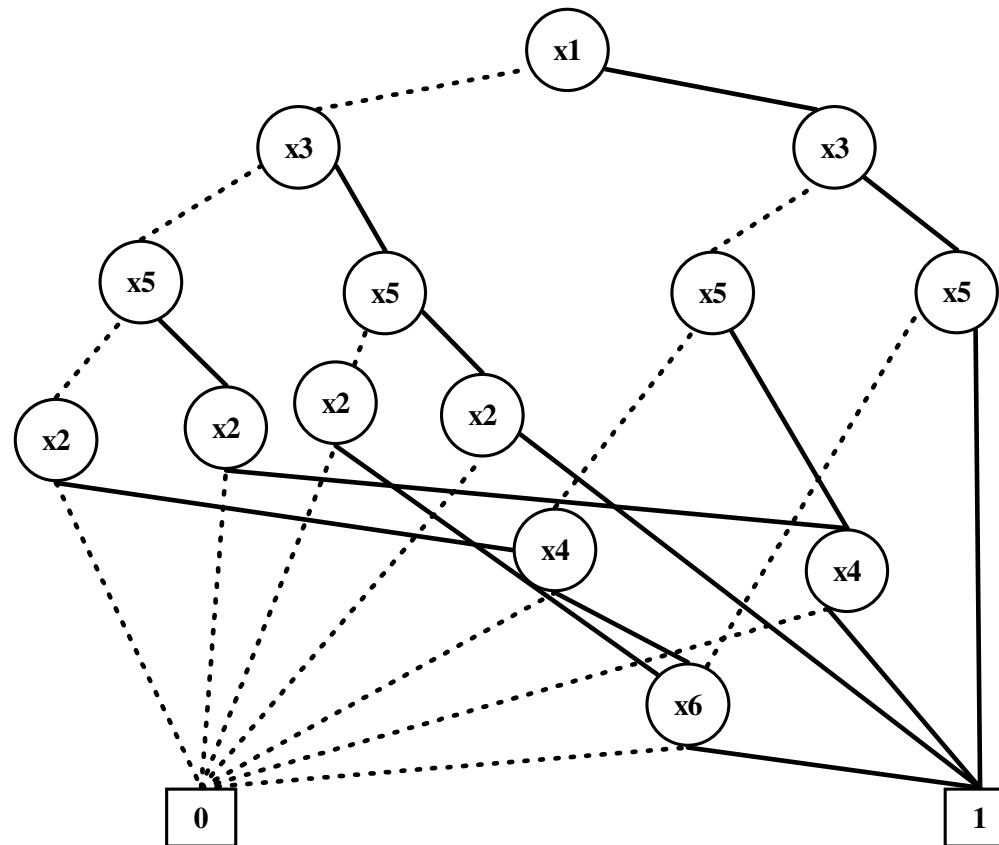
- If we chose the variable ordering $[x_1, x_2, x_3, x_4, \dots]$, then we can represent this function as an OBDD with $2n+2$ nodes.
- If we chose the variable ordering $[x_1, x_3, x_5, \dots, x_{2n-1}, x_2, x_4, x_6, \dots, x_{2n}]$, the resulting OBDD requires 2^{n+1} nodes.

OBDDs



$$f = (x_1 + x_2).(x_3 + x_4).(x_5 + x_6)$$

OBDDs



$$f = (x_1 + x_2) \cdot (x_3 + x_4) \cdot (x_5 + x_6)$$

Reduced ODBBs (ROBDDs)

A BDD is said to be reduced if none of the reduction rules R1-R3 can be applied (i.e., no more reductions are possible)

A OBDD is said to be reduced OBDD (ROBDD) if none of the reduction rules R1-R3 can be applied (i.e., no more reductions are possible)

Algorithm reduce

- The algorithm reduce provides the ROBDD of a given OBDD.
- If the ordering of B is $[x_1, x_2, \dots, x_l]$, then B has at most $l+1$ layers.
- The algorithm reduce traverses B layer by layer in a bottom-up fashion.

Algorithm reduce

- We assign an integer label $id(n)$ to each node of B .
- $id(n)$ equals to $id(m)$ iff, the subOBDDs with root nodes n and m denote the same Boolean function.

Algorithm reduce

- Given a non-terminal node n in a BDD, we define $lo(n)$ to be the node pointed to via the dashed line from n .
- Dually, $hi(n)$ is the node pointed to via the solid line from n .

Algorithm reduce

- Labeling of terminal nodes:
 - Assign the first label (say #0) to the first 0-node it encounters.
 - All other terminal 0-nodes denote the same function as the first 0-node and therefore get the same label.
 - Similarly, the 1-nodes all get the next label (say #1)
- Reduction Rule (eliminate duplicate terminals)

Algorithm reduce

- Labeling of non-terminal nodes (Given an x_i node n and already assigned integer labels to all nodes of a layer $> i$):
 - If the label $id(lo(n))$ is same as $id(hi(n))$, then we set $id(n)$ to be that label
 - (Reduction Rule:, Redundant nodes).

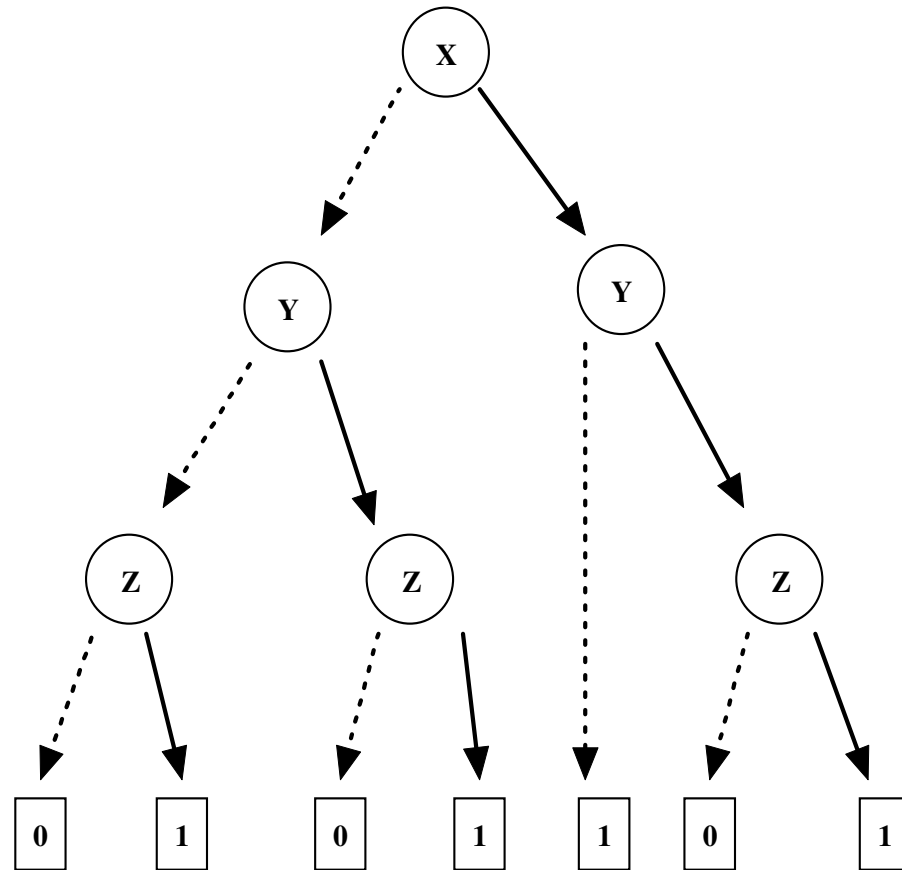
Algorithm reduce

- Labeling of non-terminal nodes (Given an x_i node n and already assigned integer labels to all nodes of a layer $> i$):
 - If there is another node m such that n and m have the same variables x_i , and **$\text{id}(\text{lo}(n)) = \text{id}(\text{lo}(m))$** and **$\text{id}(\text{hi}(n)) = \text{id}(\text{hi}(m))$** , then we set $\text{id}(n)$ to be $\text{id}(m)$.
 - (Reduction Rule, duplicate nodes)

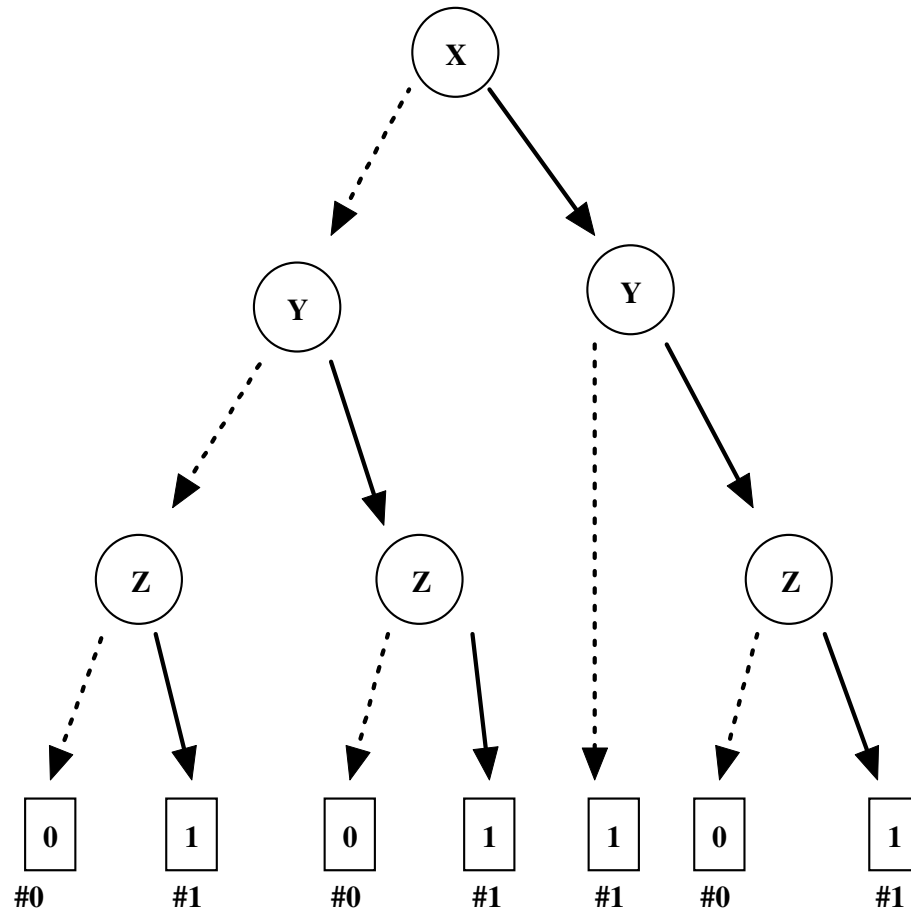
Algorithm reduce

- Labeling of non-terminal nodes (Given an x_i node n and already assigned integer labels to all nodes of a layer $> i$):
 - Otherwise, we set $id(n)$ to the next unused integer label.

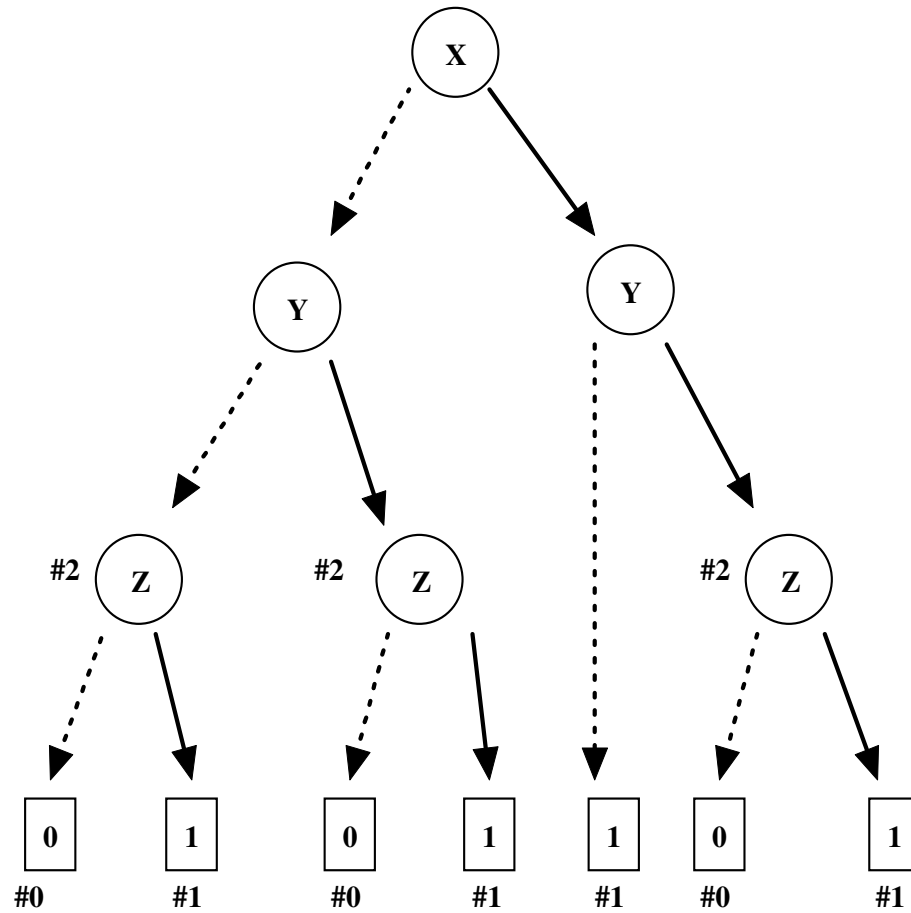
Algorithm reduce



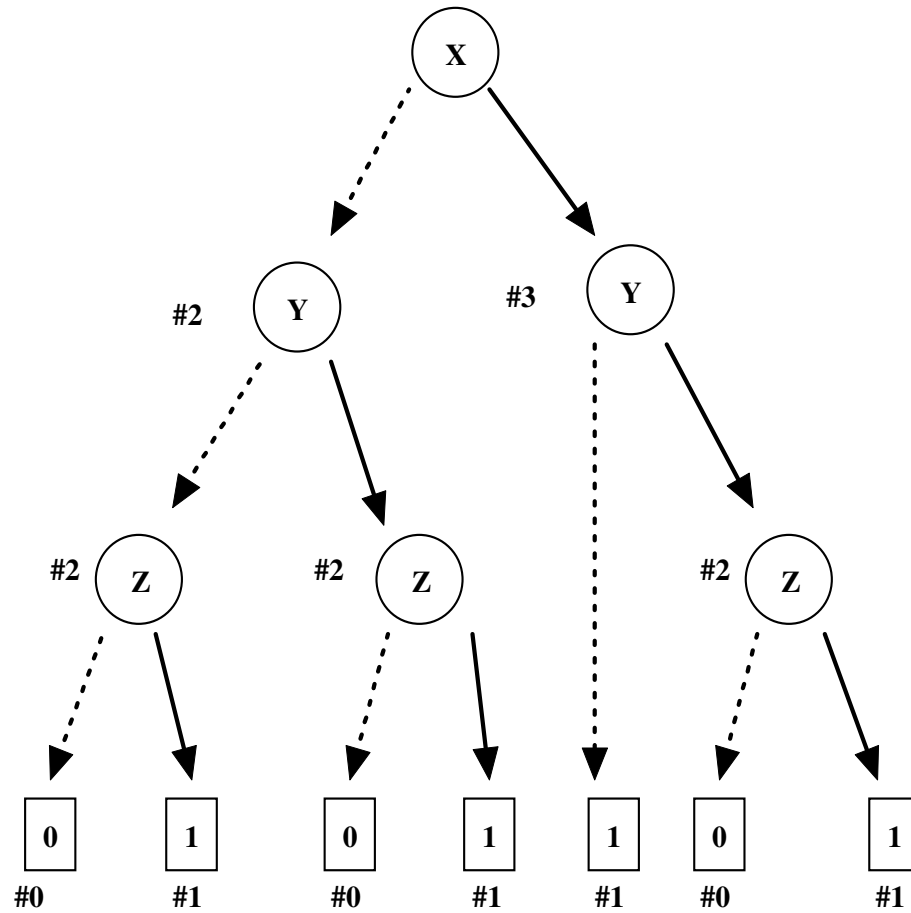
Algorithm reduce



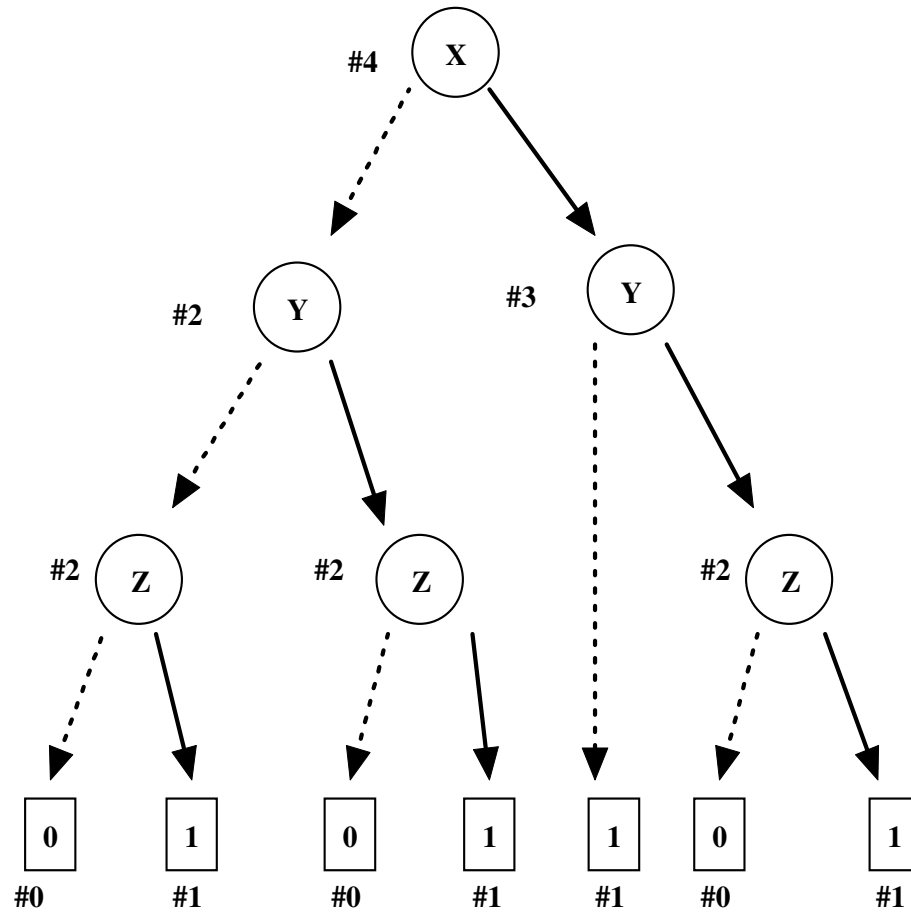
Algorithm reduce



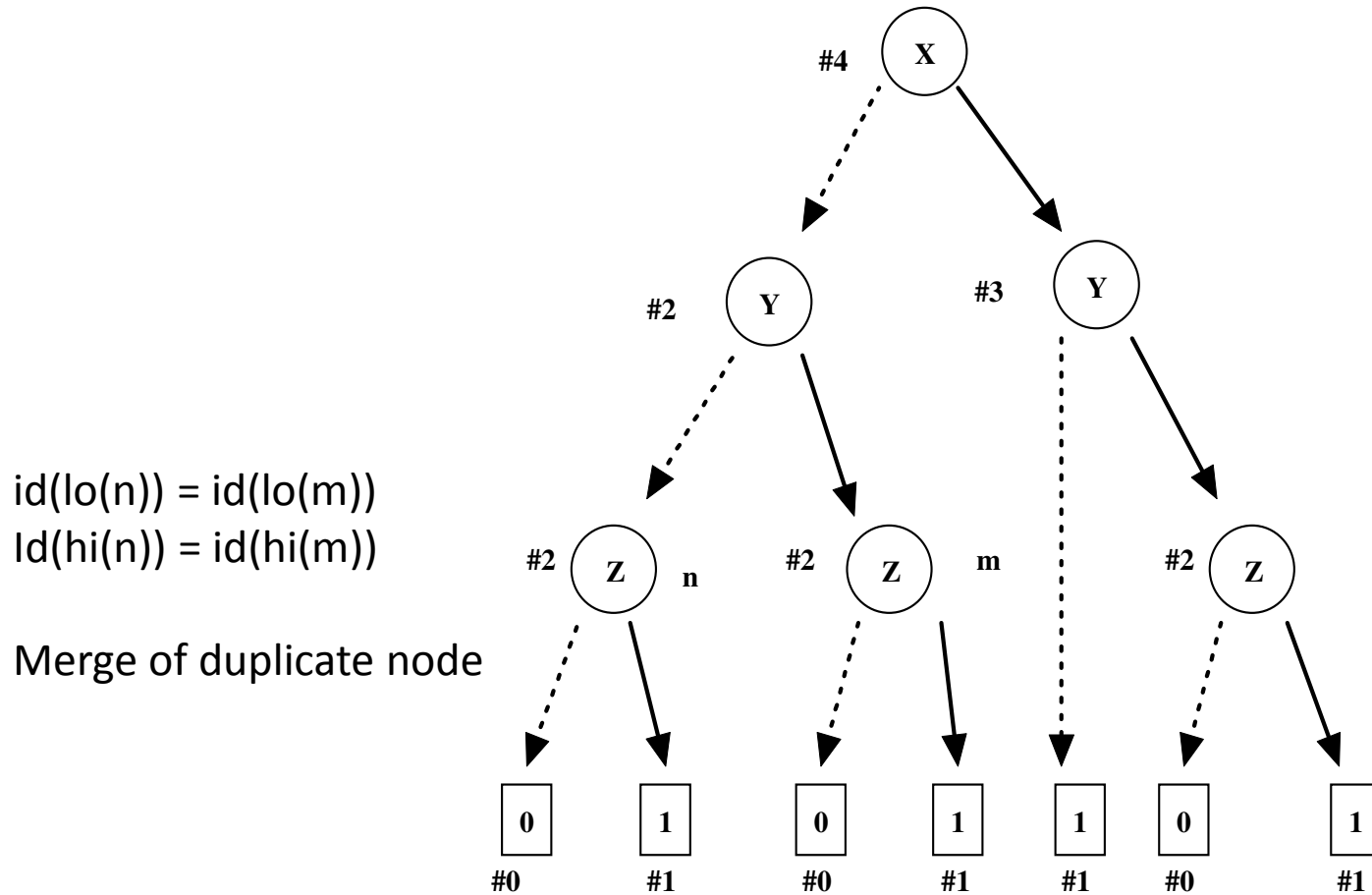
Algorithm reduce



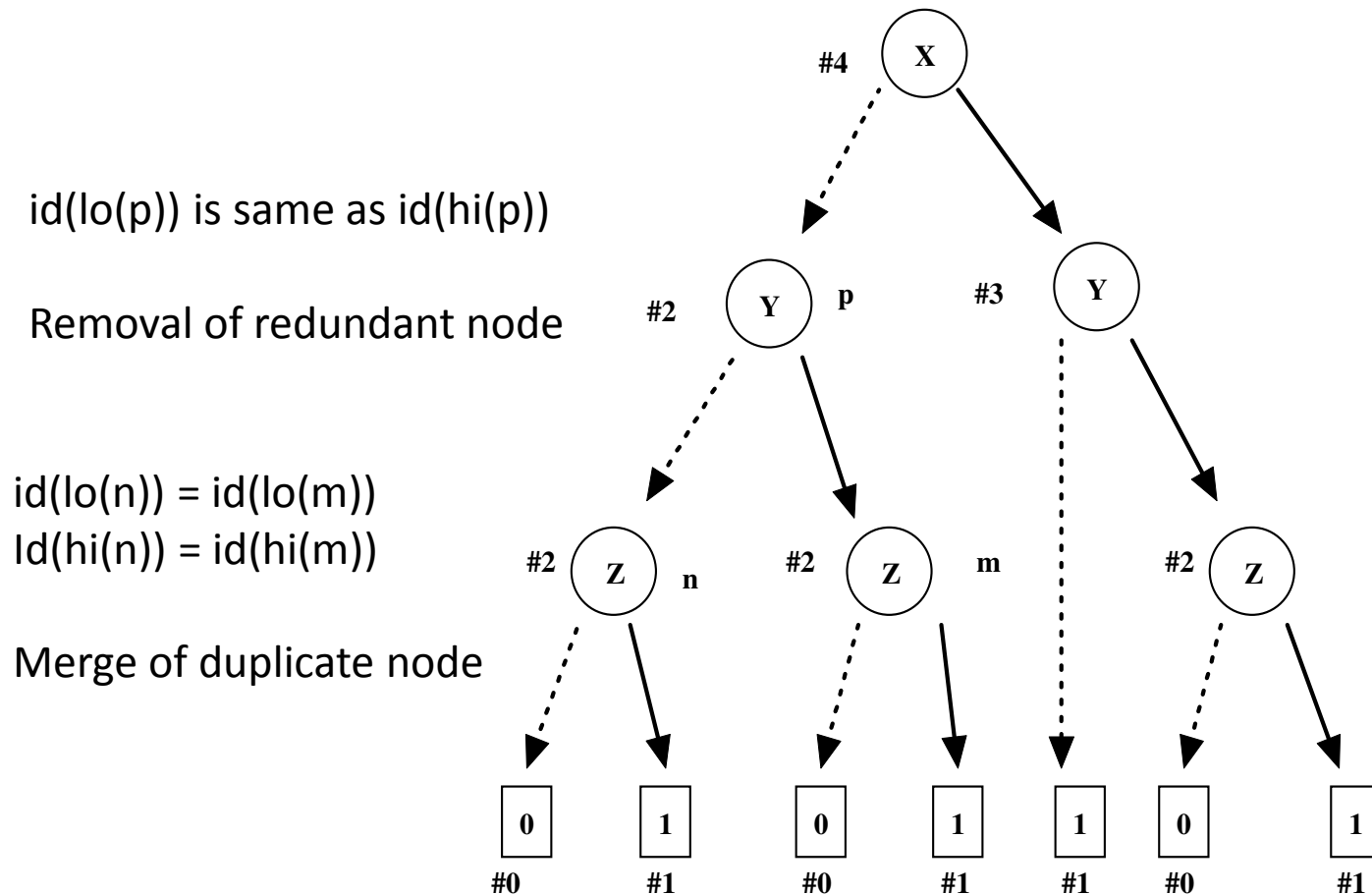
Algorithm reduce



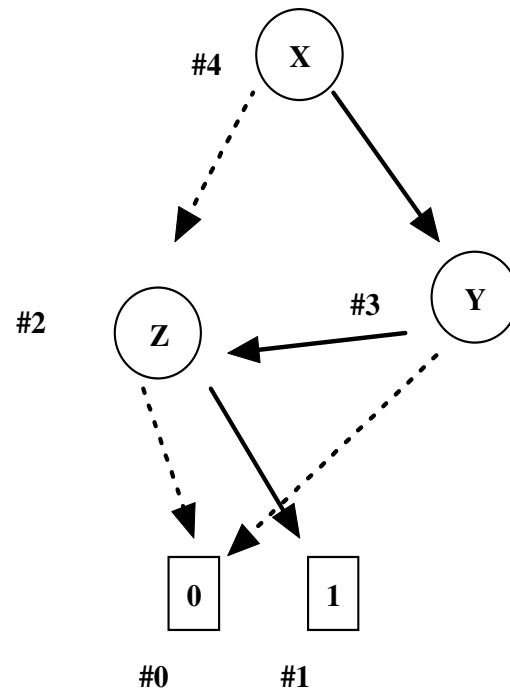
Algorithm reduce



Algorithm reduce



Algorithm reduce



Reduced Ordered BDD (ROBDD)

Reduced Ordered BDDs (ROBDDs)

- The reduced OBDD, representing a given function f , is *unique*.
- That is to say, let B_1 and B_2 be two reduced OBDDs with *compatible variable ordering*. If B_1 and B_2 represent the same Boolean function, then they have identical structure.
- The order in which we applied the reductions does not matter.
- OBDDs have a canonical form, their unique ROBDDs.

Reduced Ordered BDDs (ROBDDs)

Let B_1 and B_2 are the BDDs of Boolean function f_1 and f_2 .

The orderings of B_1 and B_2 are said to be ***compatible*** if there are no variables x and y such that x comes before y in the ordering of B_1 and y comes before x in the ordering of B_2 .

Application of BDDs

- **Test for Absence of redundant variables**
 - If the value of a Boolean function $f(x_1, x_2, \dots, x_n)$ does not depend on the value x_i , then any ROBDD which represents f does not contain any x_i -node.

Application of BDDs

- **Test for semantic equivalence**
 - B_f and B_g are the ROBDD representation of two functions f and g respectively with compatible variable ordering.
 - f and g denote the same Boolean function if, and only if, the ROBDDs have identical structure.

Application of BDDs

- **Test for Validity**
 - Consider the ROBDD of a Boolean function $f(x_1, x_2, \dots, x_n)$.
 - f is valid if, and only if, its ROBDD is B_1 .

Application of BDDs

- **Test for Implication ($f \rightarrow g$)**
 - We can test whether f implies g by computing the ROBDD of $(B_f \wedge \neg B_g)$
 - f implies g if, and only if, the resultant ROBDD of $(B_f \wedge \neg B_g)$ is B_0

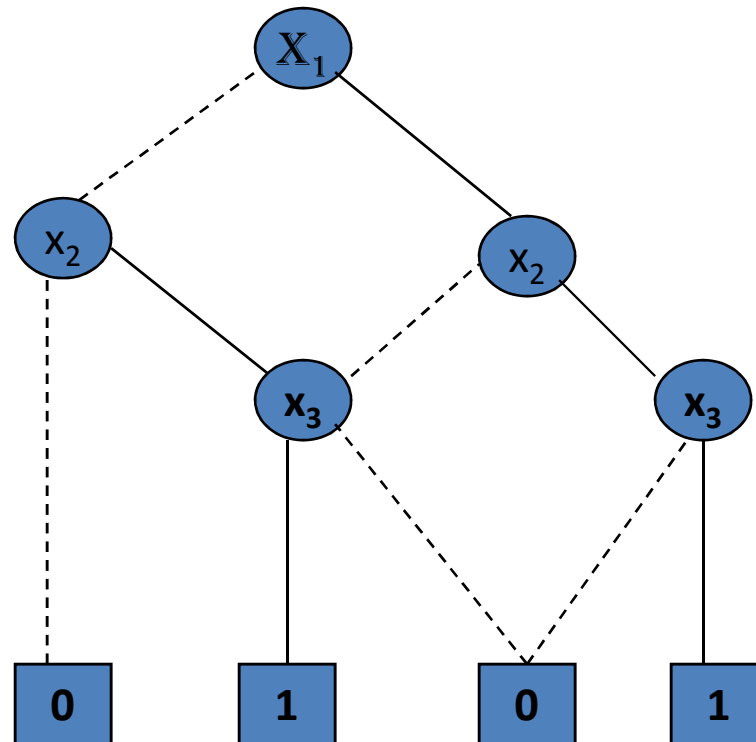
Application of BDDs

- **Test for Satisfiability**

- A Boolean function $f(x_1, x_2, \dots, x_n)$ is satisfiable if it computes 1 for at least one assignment of 0 and 1 values to its variables.
- The function f is satisfiable if, and only if, its ROBDD is not B_0 .

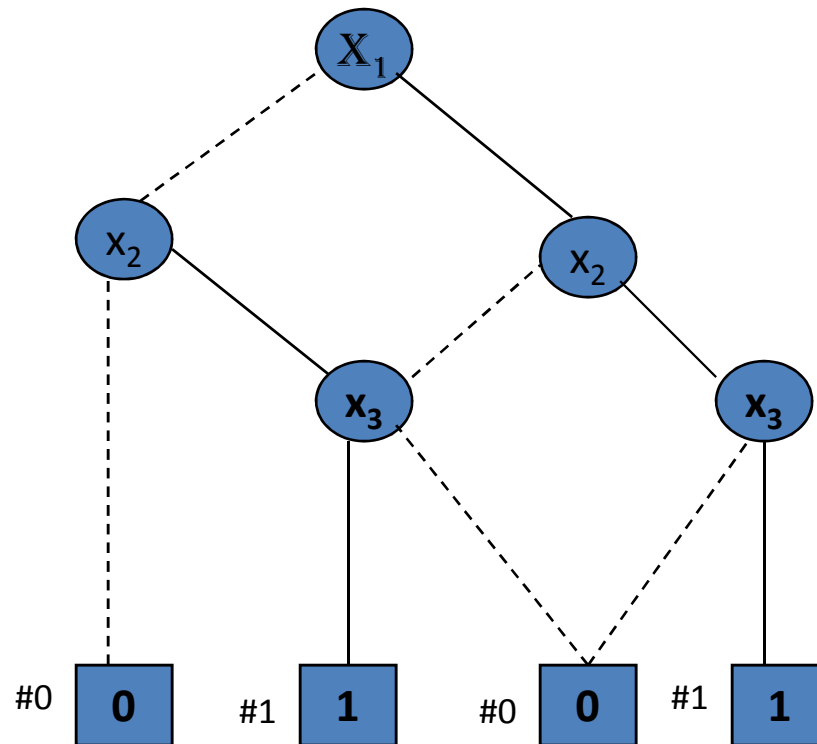
Question

- Apply the algorithm reduce



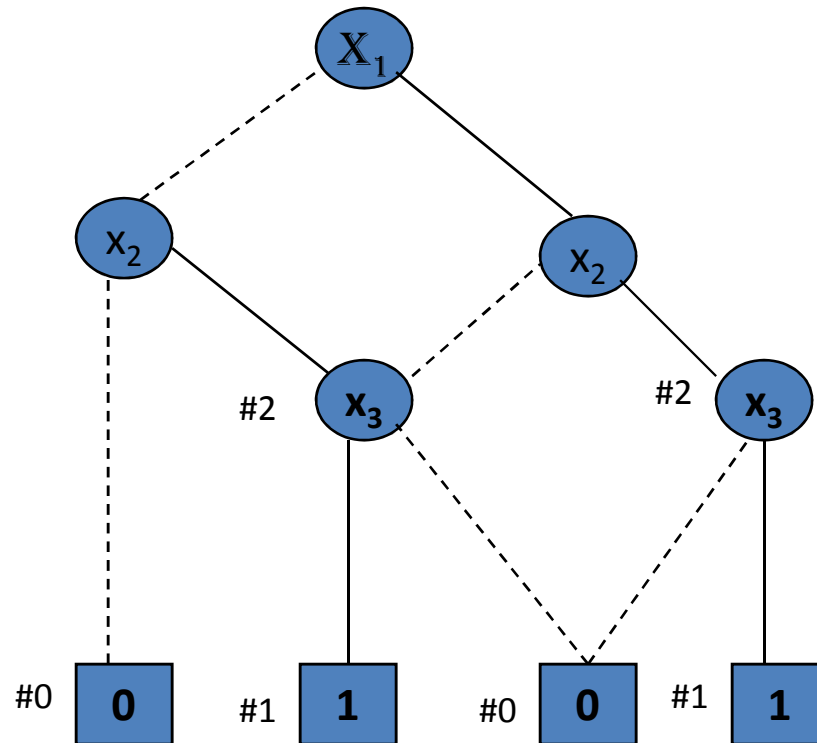
Question

- Apply the algorithm reduce



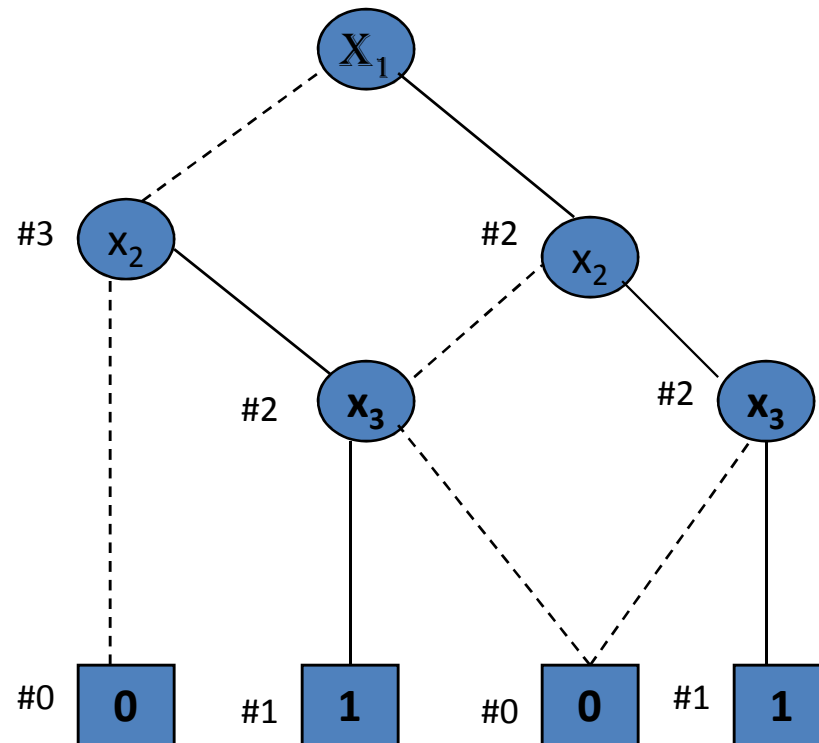
Question

- Apply the algorithm reduce



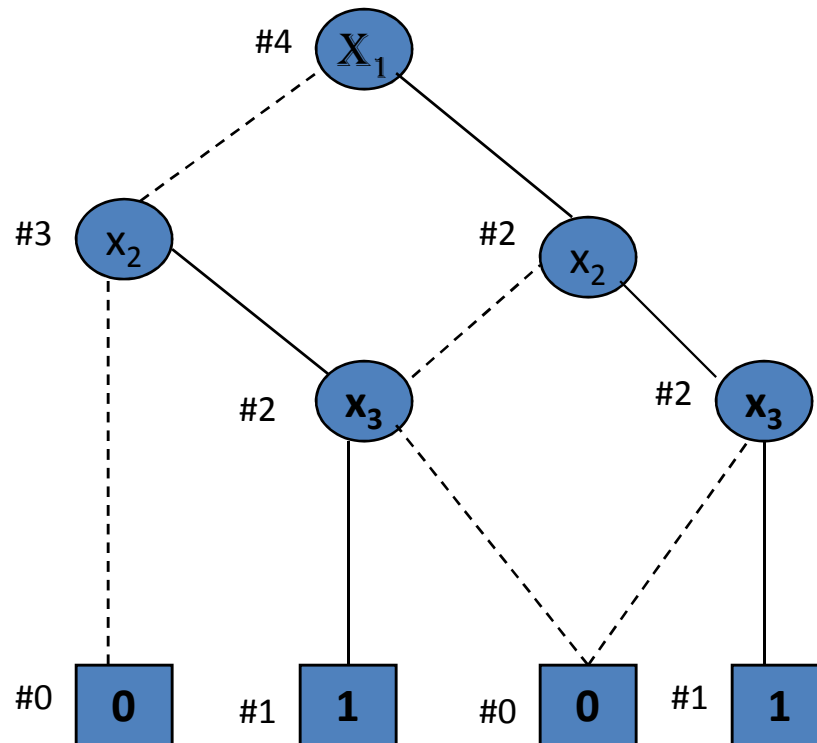
Question

- Apply the algorithm reduce



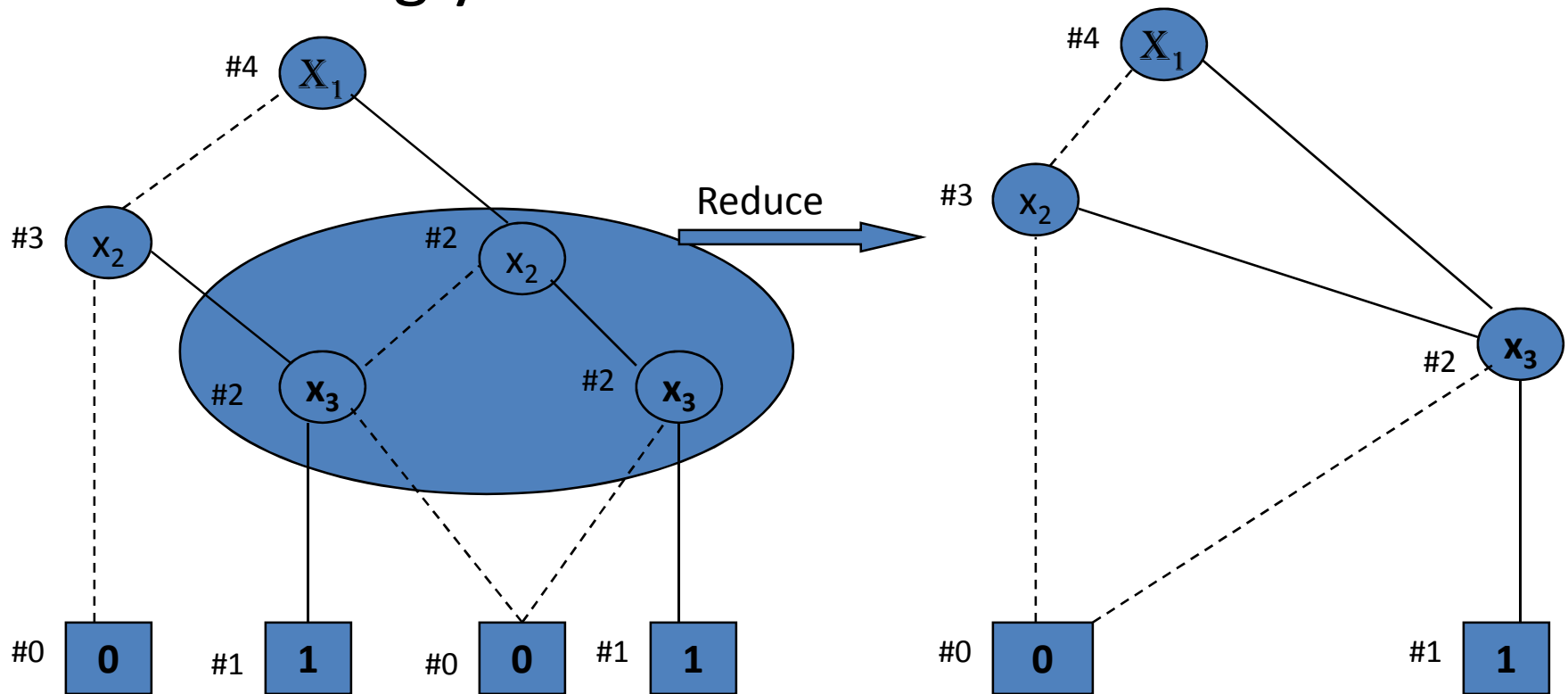
Question

- Apply the algorithm reduce



Algorithm *reduce* for BDDs

- Merge all nodes which have same label and redirect the incoming and outgoing edges accordingly.



Question

- Consider the following function
 - $f(x,y,z) = xz + xz' + x'y$
 - Is it independent of any variables.

Question

- Consider the following function

$$- f(x,y,z) = xz + xz' + x'$$

Is it independent of any variables.

Test for validity

Question

NPTEL Phase-II
Video course on

**Design Verification and Test of
Digital VLSI Designs**

Dr. Santosh Biswas
Dr. Jatindra Kumar Deka
IIT Guwahati

Module VI: Binary Decision Diagram

Lecture III: : Operation on Ordered Binary Decision Diagram

Operation on BDD

- $f+g$
- $f.g$

Operation on BDD

Operation on OBDD

Algorithm apply

To perform the binary operation on two ROBDD's B_f and B_g , corresponding to the functions f and g respectively, we use the algorithm *apply*(**op**, B_f , B_g). The two ROBDDs B_f and B_g have compatible variable ordering.

Operation on OBDD

Algorithm apply

Application of *apply*(*op*, B_f , B_g) will give a OBDD. The ordering of the resultant BDD is same as B_f or B_g but it may not be the reduced one. After constructing the resultant BDD, we may apply the reduce algorithm to get the ROBDD.

Operation on OBDD

Operation on OBDD

The function *apply* is based on the Shannon's expansion for f and g :

$$f = \bar{x}.f[0/x] + x.f[1/x]$$

$$g = \bar{x}.g[0/x] + x.g[1/x]$$

From the Shannon's expansion of f and g :

$$f \text{ op } g = \bar{x}.(f[0/x] \text{ op } g[0/x]) + x.(f[1/x] \text{ op } g[1/x])$$

Operation on OBDD

This is used as a control structure of apply which proceeds from the roots of B_f and B_g downwards to construct nodes of the OBDD $B_f \text{ op } B_g$.

Let r_f be the root node of B_f and r_g be the root node of B_g .

Operation on OBDD

Algorithm apply(op, B_f , B_g)

1. If both r_f and r_g are terminal nodes with labels l_f and l_g , respectively compute the value l_f op l_g and the resulting OBDD is B_0 if the value is 0 and B_1 otherwise.

Operation on OBDD

In the remaining cases, at least one of the root nodes is a non-terminal.

If both nodes are x_i -nodes (i.e., non-terminal of same variable),
create an x_i -node n (called r_f, r_g) with
a dashed line to *apply* ($op, lo(r_f),$
 $lo(r_g)$) and a solid line to *apply*($op,$
 $hi(r_f), hi(r_g)$).

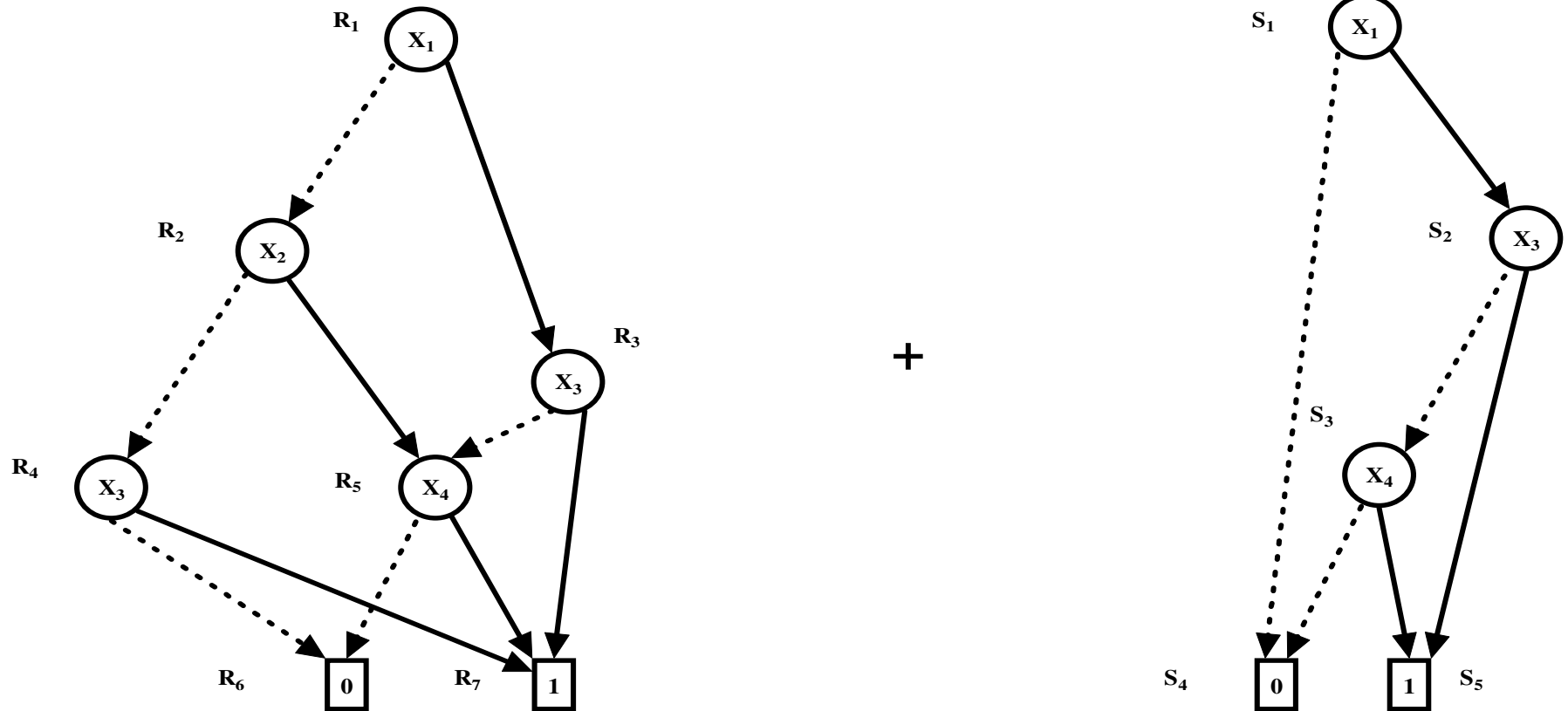
Operation on OBDD

If r_f is an x_i -node, but r_g is a terminal node or an x_j -node with $j > i$,
create an x_i -node n (called r_f, r_g) with
a dashed line to $\mathit{apply}(op, lo(r_f), r_g)$
and a solid line to $\mathit{apply}(op, hi(r_f),$
 $r_g)$.

Operation on OBDD

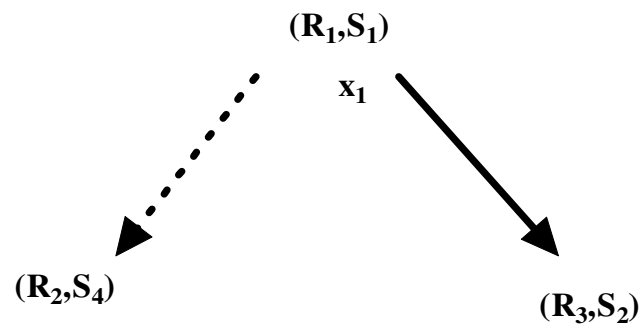
If r_g is an x_i -node, but r_f is a terminal node or an x_j -node with $j > i$,
create an x_i -node n (called r_f, r_g) with
a dashed line to $\mathit{apply}(op, lo(r_g), r_f)$
and a solid line to $\mathit{apply}(op, hi(r_g), r_f)$.

Operation on OBDD

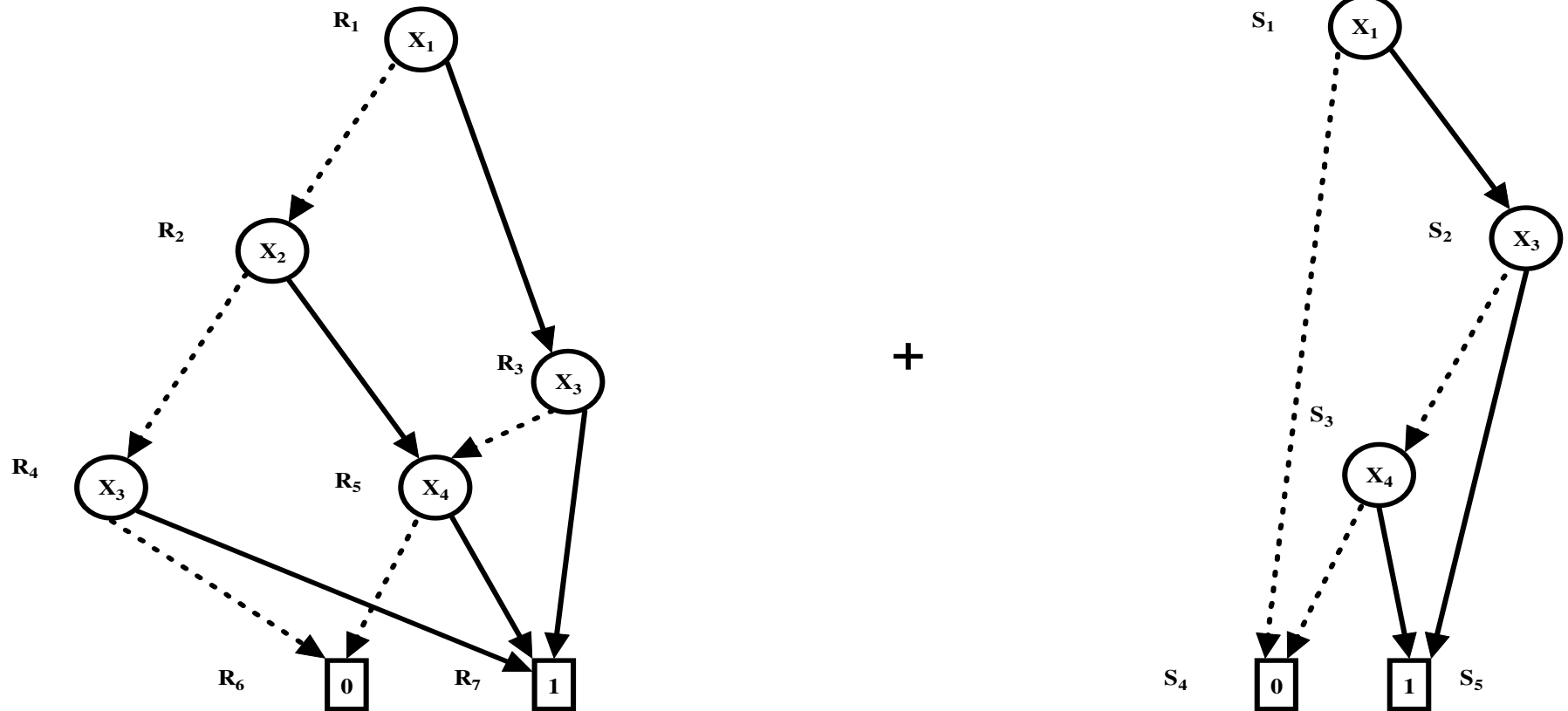


Variable ordering: $[x_1, x_2, x_3, x_4]$

Operation on OBDD



Operation on OBDD

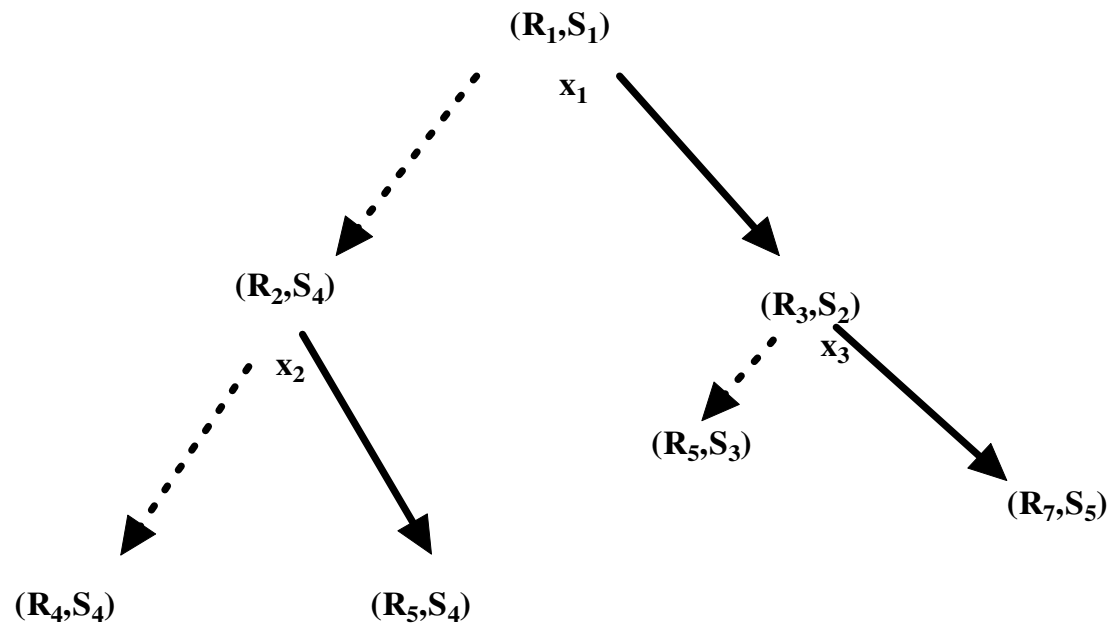


Variable ordering: $[x_1, x_2, x_3, x_4]$

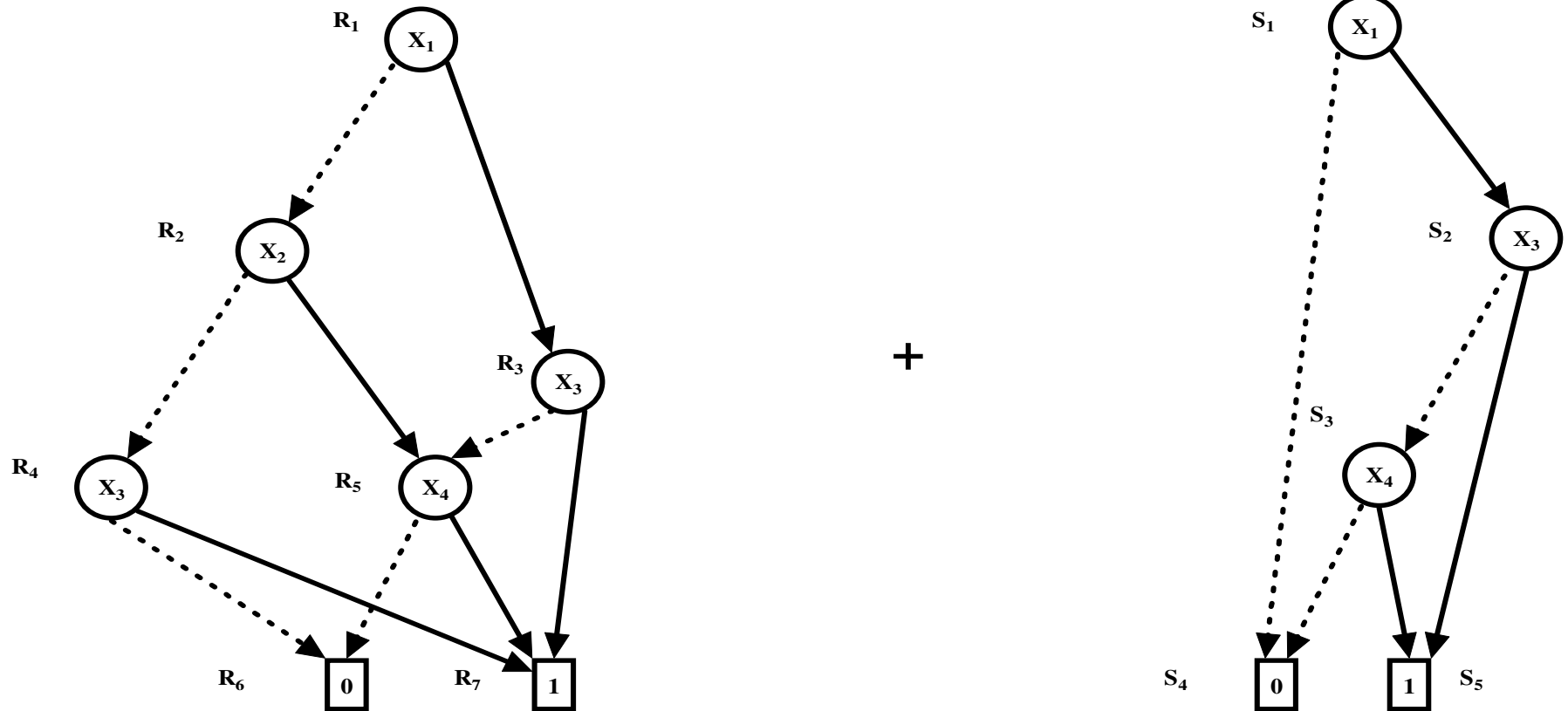
Operation on OBDD

If r_f is an x_i -node, but r_g is a terminal node or an x_j -node with $j > i$,
create an x_i -node n (called r_f, r_g) with
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and a solid line to $\mathit{apply}(op, hi(r_f), r_g)$.

Operation on OBDD

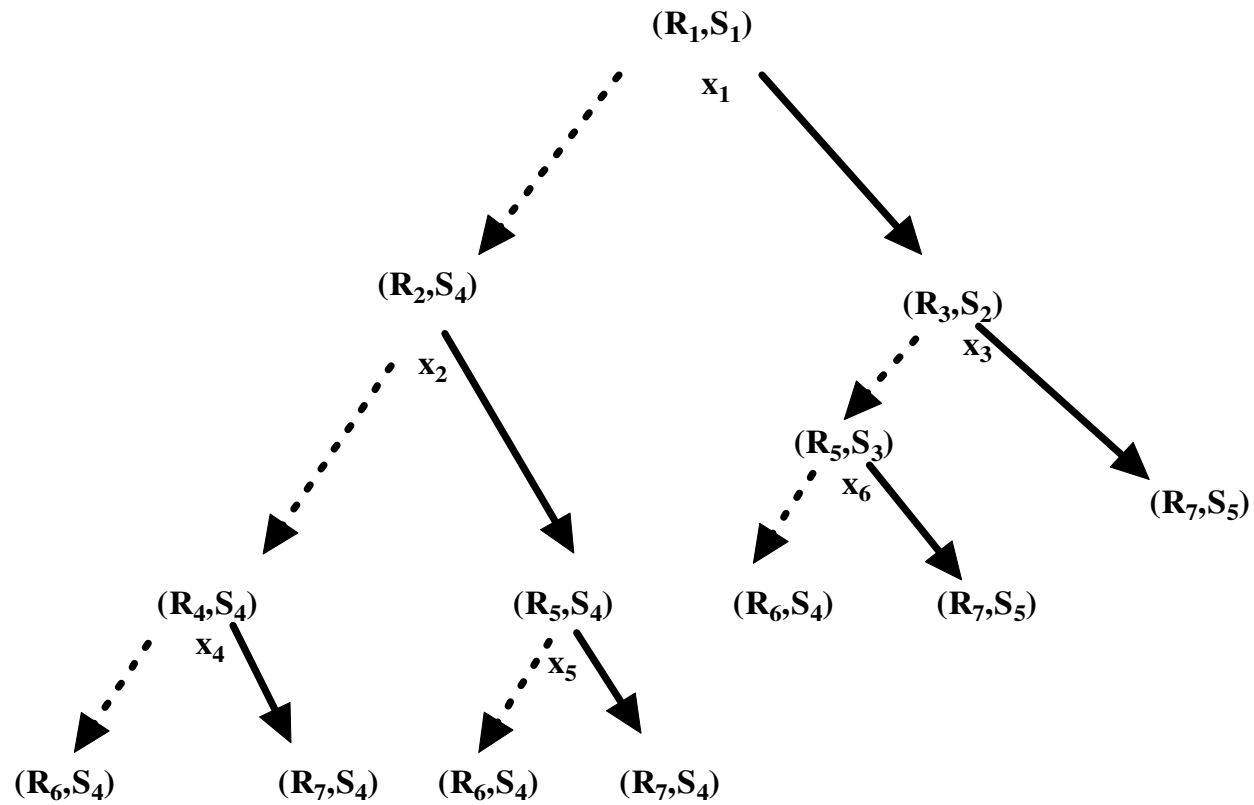


Operation on OBDD

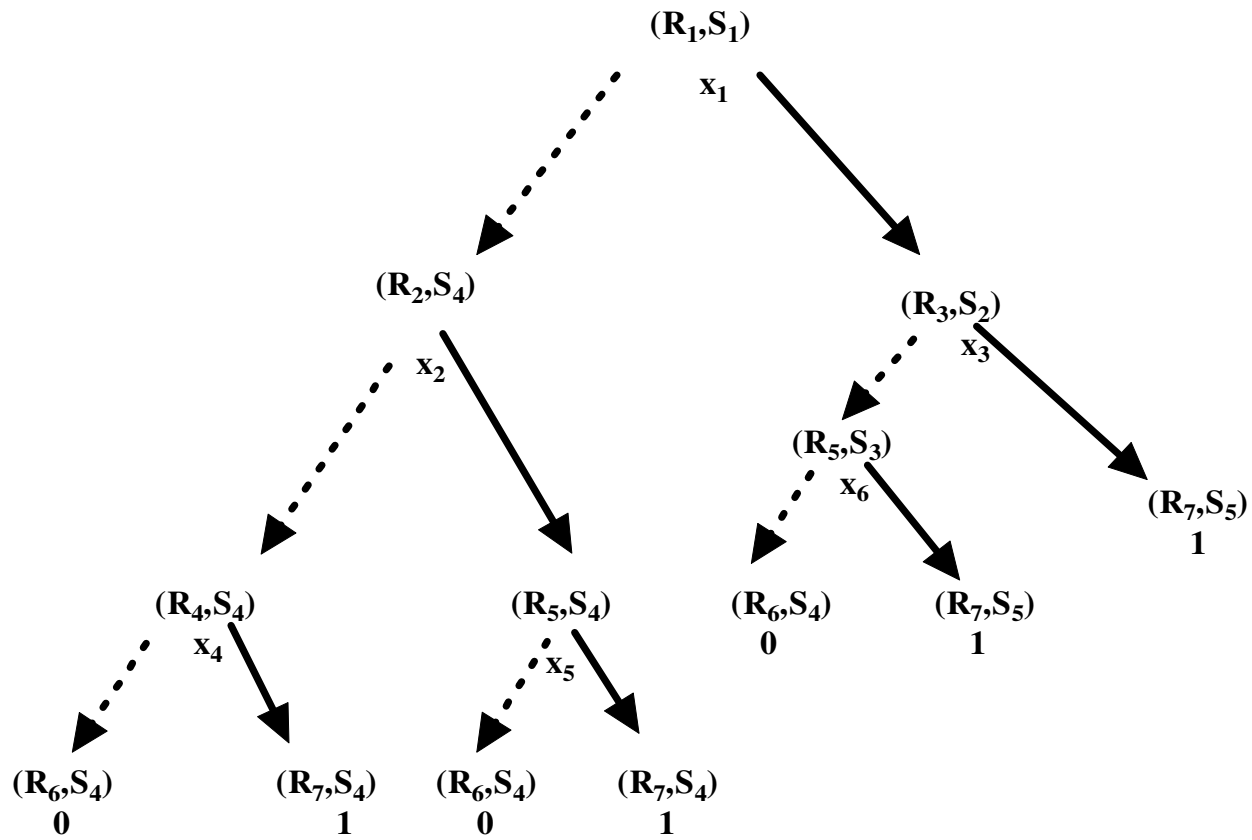


Variable ordering: $[x_1, x_2, x_3, x_4]$

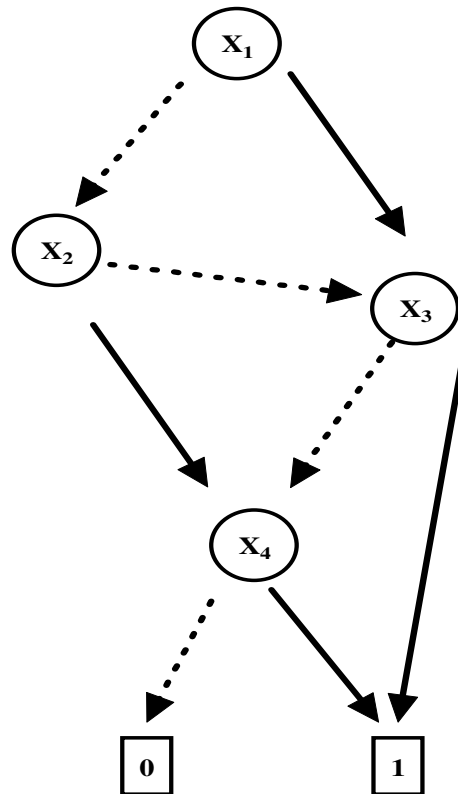
Operation on OBDD



Operation on OBDD



Operation on OBDD



Operation on OBDD

Algorithm restrict

The Boolean formula obtained by replacing all occurrences of x in f by 0 is denoted by $f[0/x]$.

The formula $f[1/x]$ is defined similarly.

The expressions $f[0/x]$ and $f[1/x]$ are called restriction of f .

Operation on OBDD

restrict(0, x, B_f)

For each node n corresponding to x ,
remove n from OBDD and redirect
incoming edges to $lo(n)$

restrict(1, x, B_f)

For each node n corresponding to x ,
remove n from OBDD and redirect
incoming edges to $hi(n)$

Operation on OBDD

Sometimes we need to express relaxation of the constraint on a subset of variables.

If we relax the constraint on some variable x of a Boolean function f , then f could be made true by putting x to 0 or to 1.

Operation on OBDD

We write $(\exists x.f)$ for the Boolean function f with the constraint on x relaxed and it can be expressed as:

$$\exists x.f = f[0/x] + f[1/x]$$

i.e., there exists x on which the constraint is relaxed.

Operation on OBDD

Algorithm exists

The *exists* algorithm can be implemented in terms of the algorithms *apply* and *restrict* as

$$\exists x.f = \text{apply}(+, \text{restrict}(0, x, B_f), \text{restrict}(1, x, B_f))$$

Operation on OBDD

Algorithm exists

The exists operation can be easily generalized to a sequence of exists operations

$$\exists x_1. \exists x_2. \dots \exists x_n. f$$

.

Question

- Consider the following function

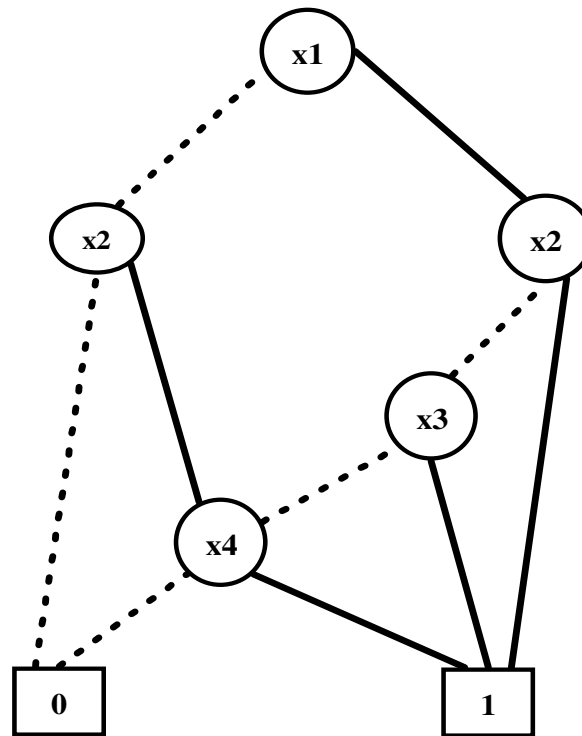
$$f = x_1'x_2x_4 + x_1x_2'x_3 + x_1x_2'x_3'x_4 + x_1x_2$$

Construct the ROBDD for f : B_f

$\text{restrict}(0, x_4, B_f)$ and $\text{restrict}(1, x_4, B_f)$

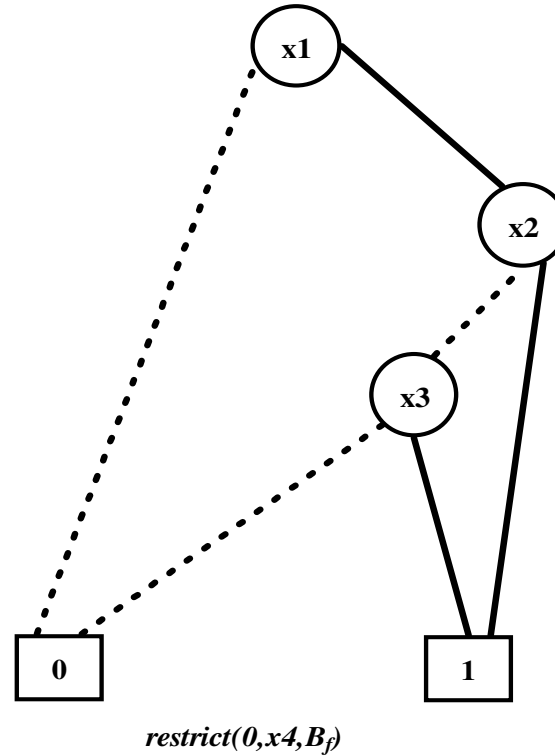
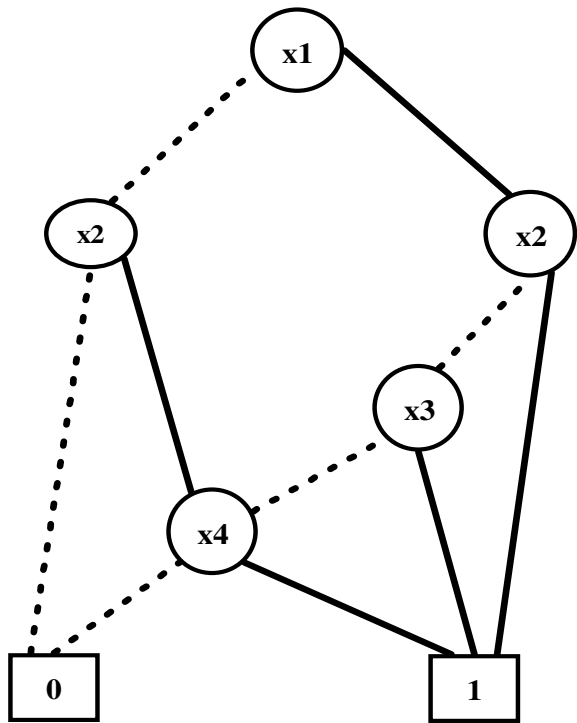
$\text{exists}(x_4, B_f)$

Question



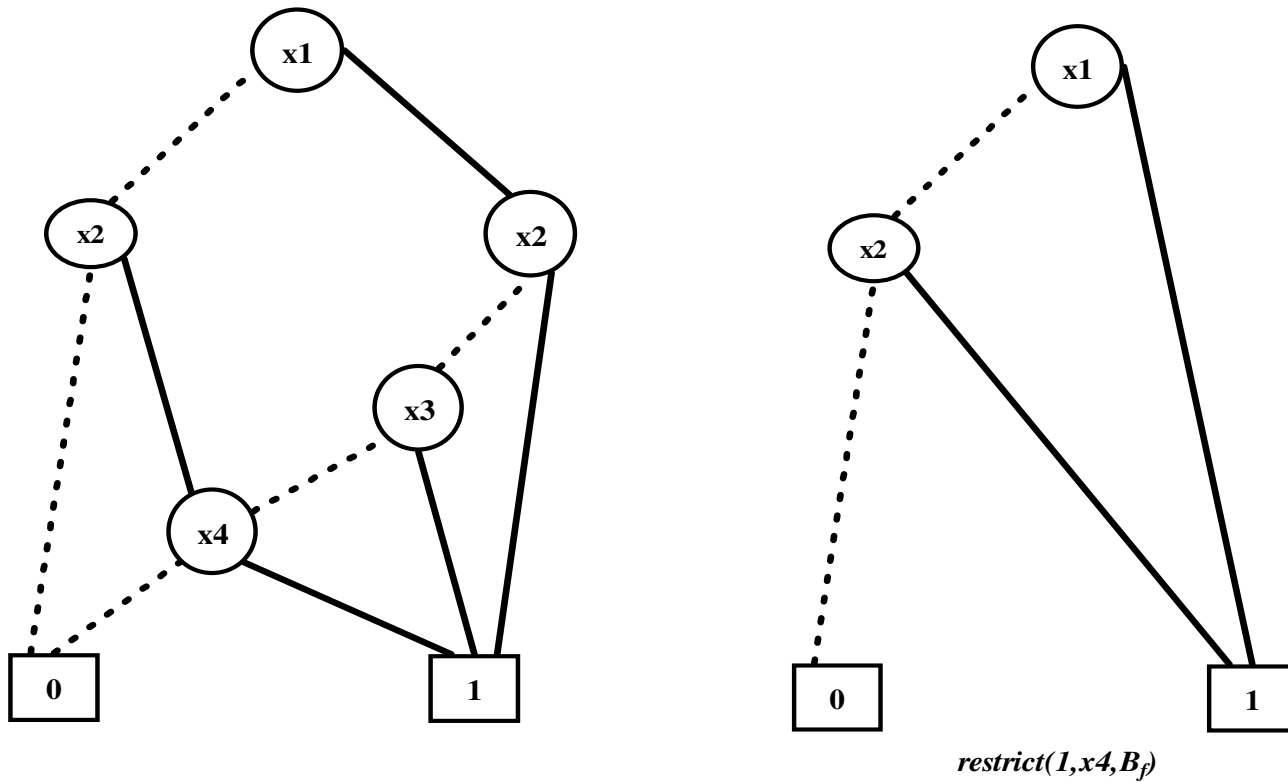
$$f = x1'x2x4 + x1x2'x3 + x1x2'x3'x4 + x1x2$$

Question



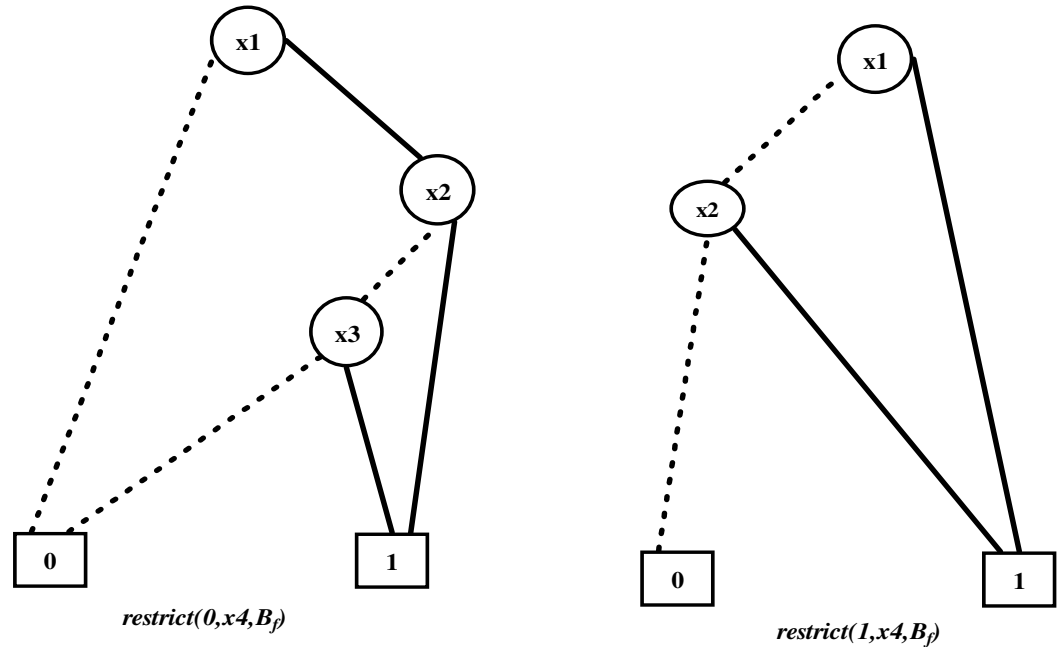
$$f = x_1'x_2x_4 + x_1x_2'x_3 + x_1x_2'x_3'x_4 + x_1x_2$$

Question



$$f = x1'x2x4 + x1x2'x3 + x1x2'x3'x4 + x1x2$$

Question

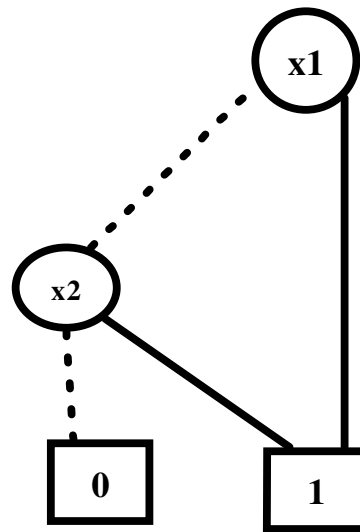


$$f = x_1'x_2x_4 + x_1x_2'x_3 + x_1x_2'x_3'x_4 + x_1x_2$$

Exists x_4 $f = \text{apply}(+, \text{restrict}(0, x_4, B_f), \text{restrict}(1, x_4, B_f))$

Question

Question



Question

- Show that the formula $\exists x.f$ depends on all those variables that f depends upon, except x .
- If f computes to 1 with respect to a valuation ν , then $\exists x.f$ computes 1 with respect to the same valuation.

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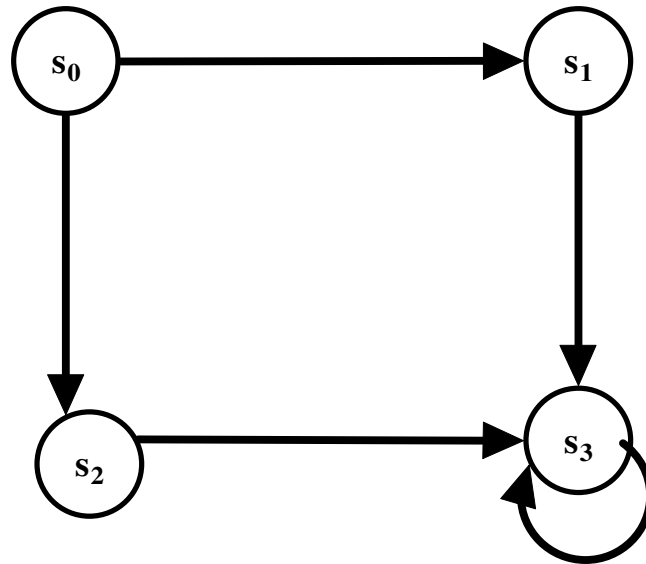
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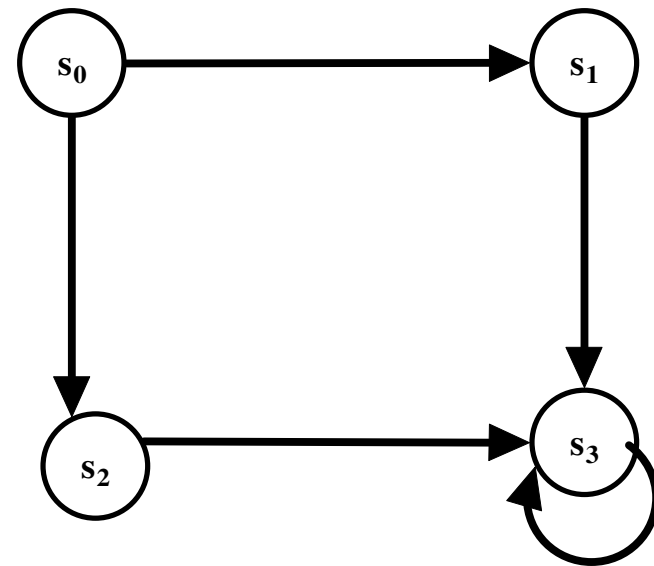
Module VI: Binary Decision Diagram

Lecture IV: Ordered Binary Decision Diagram for
State Transition Systems

State Transition System



State Transition System



the states s_0 , s_1 , s_2 and s_3 can be distinguished using two state variables, say x_1 and x_2 .

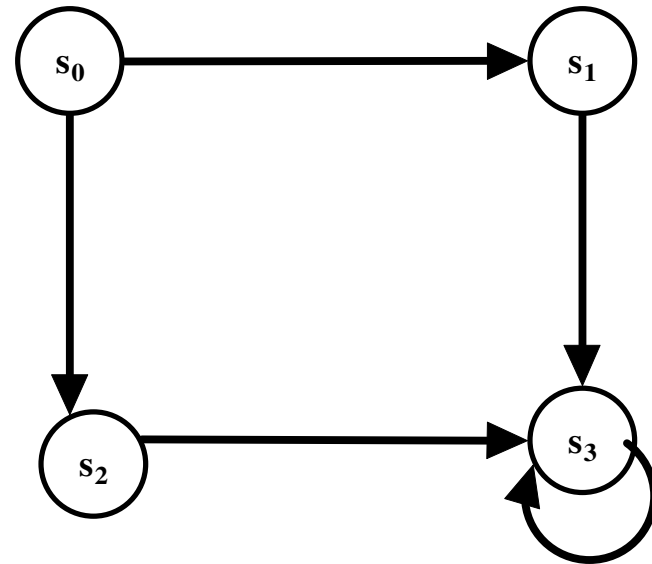
State Transition System

$$\{s_0\} = \overline{x_1} \overline{x_2}$$

$$\{s_1\} = \overline{x_1} x_2$$

$$\{s_2\} = x_1 \overline{x_2}$$

$$\{s_3\} = x_1 x_2$$



the states s_0, s_1, s_2 and s_3 can be distinguished using two state variables, say x_1 and x_2 .

State Transition System: set of states

- Set of states

State Transition System: set of states

$$\{s_0, s_1\} = \overline{x_1 x_2} + \overline{x_1} x_2$$

$$\{s_0, s_2\} = \overline{x_1 x_2} + x_1 \overline{x_2}$$

$$\{s_0, s_3\} = \overline{x_1 x_2} + x_1 x_2$$

$$\{s_1, s_2\} = \overline{x_1} x_2 + x_1 \overline{x_2}$$

$$\{s_1, s_3\} = \overline{x_1} x_2 + x_1 x_3$$

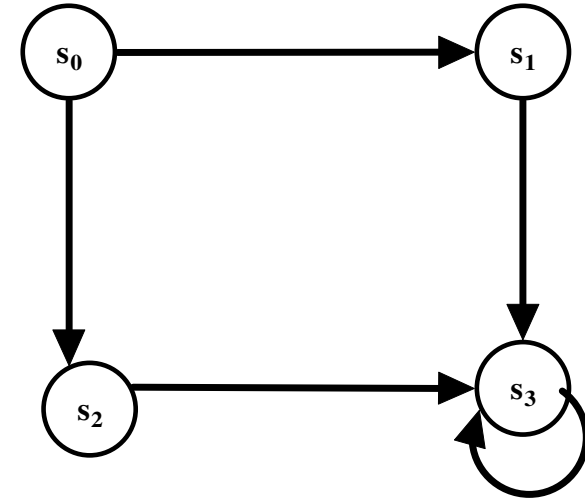
$$\{s_2, s_3\} = x_1 \overline{x_2} + x_1 x_3$$

$$\{s_0, s_1, s_2\} = \overline{x_1 x_2} + \overline{x_1} x_2 + x_1 \overline{x_2}$$

$$\{s_0, s_1, s_3\} = \overline{x_1 x_2} + \overline{x_1} x_2 + x_1 x_3$$

$$\{s_0, s_2, s_3\} = \overline{x_1 x_2} + x_1 \overline{x_2} + x_1 x_3$$

$$\{s_0, s_1, s_2, s_3\} = \overline{x_1 x_2} + \overline{x_1} x_2 + x_1 \overline{x_2} + x_1 x_3$$



$$\{s_0\} = \overline{x_1 x_2}$$

$$\{s_1\} = \overline{x_1} x_2$$

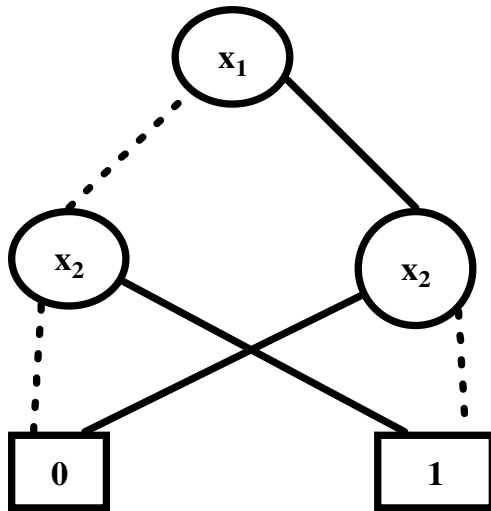
$$\{s_2\} = x_1 \overline{x_2}$$

$$\{s_3\} = x_1 x_2$$

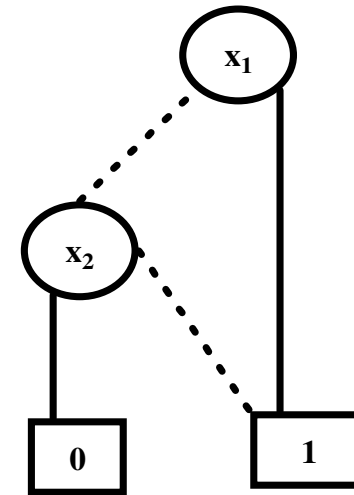
State Transition Diagram: set of states

- Set of states is represented by Boolean expression.
- OBDDs are used to represent Boolean expression.

State Transition Systems: set of states



ROBDD for $\{s_1, s_2\}$
 $x_1'x_2 + x_1x_2'$



ROBDD for $\{s_0, s_2, s_3\}$
 $x_1'x_2' + x_1x_2' + x_1x_2$

State Transition Systems: Set of states

- Set operation:
 - Union, Intersection, etc
- S_1 and S_2 are two sets.

State Transition Systems: Set of states

- Set operation:
 - Union, Intersection, etc
- S1 and S2 are two sets.
- B_{S_1} and B_{S_2} are the OBDD representation of sets S1 and S2 respectively.
- Union of S1 and S2 is $apply(+, B_{S_1}, B_{S_2})$
- Intersection of S1 and S2 is $apply(., B_{S_1}, B_{S_2})$

State Transition system: transition

- Transition of a system can be viewed as an ordered pair (s_p, s_n)
 - s_p : present state
 - s_n : next state

State Transition system: transition

- Transition of a system can be viewed as an ordered pair (s_p, s_n)
 - s_p : present state
 - s_n : next state
 - If n variables are used to represent the current state
$$x_1, x_2, x_3, x_4, \dots, x_n$$
 - We Need another n variables to represent the next state
$$x'_1, x'_2, x'_3, x'_4, \dots, x'_n$$

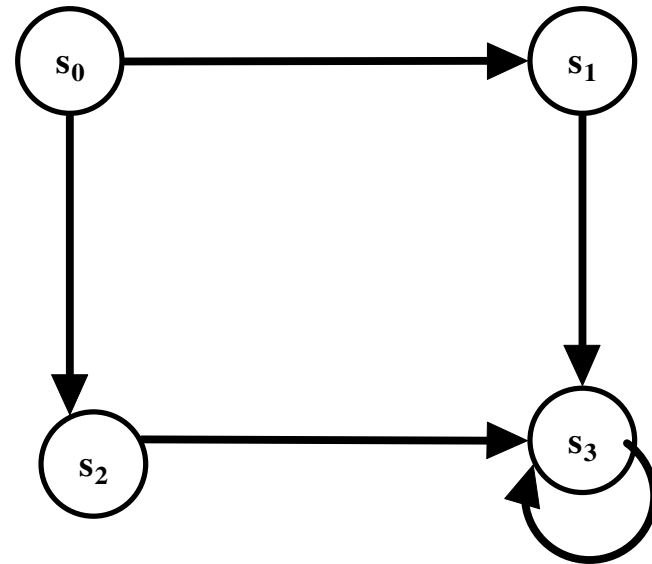
State Transition System: Transitions

$$\{s_0\} = \overline{x_1} \overline{x_2}$$

$$\{s_1\} = \overline{x_1} x_2$$

$$\{s_2\} = x_1 \overline{x_2}$$

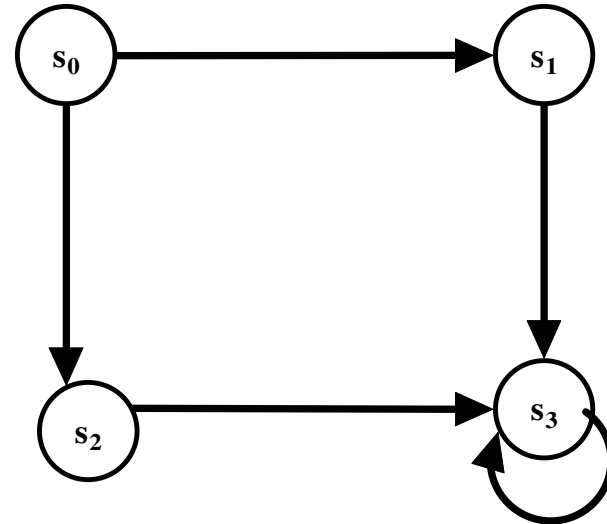
$$\{s_3\} = x_1 x_2$$



the states s_0, s_1, s_2 and s_3 can be distinguished using two state variables, say x_1 and x_2 .

State Transition System: Transitions

Next state variables: x_1' and x_2'



$$\{s_0\} = \overline{x_1} \overline{x_2}$$

$$\{s_1\} = \overline{x_1} x_2$$

$$\{s_2\} = x_1 \overline{x_2}$$

$$\{s_3\} = x_1 x_2$$

State Transition system: transition

State Transition system

- State transition system can be represented by Boolean expression.
- OBDD is used to represent Boolean expression.

Verification: Model Checking

- Model of the system: Kripke structure
 - Set of states
 - Transitions
 - Labeling function
- Specification/Property: CTL
- Verification Method: Model Checking method

Model Checking

- Graph traversal algorithm
- State space explosion problem
- OBDD can be used to represent kripke structure
 - State transition system
 - Labeling function

Model Checking

- Symbolic Model Checking

CTL Model Checking

Temporal Operator:

AF p

- If any state s is labeled with p , label it with AF p
- Repeat: label any state with AF p if all successor states are labeled with AF p until there is no change.

Symbolic Model Checking

- Requirements:
 - Find the predecessor state(s) of a state or a set of states

Symbolic Model Checking

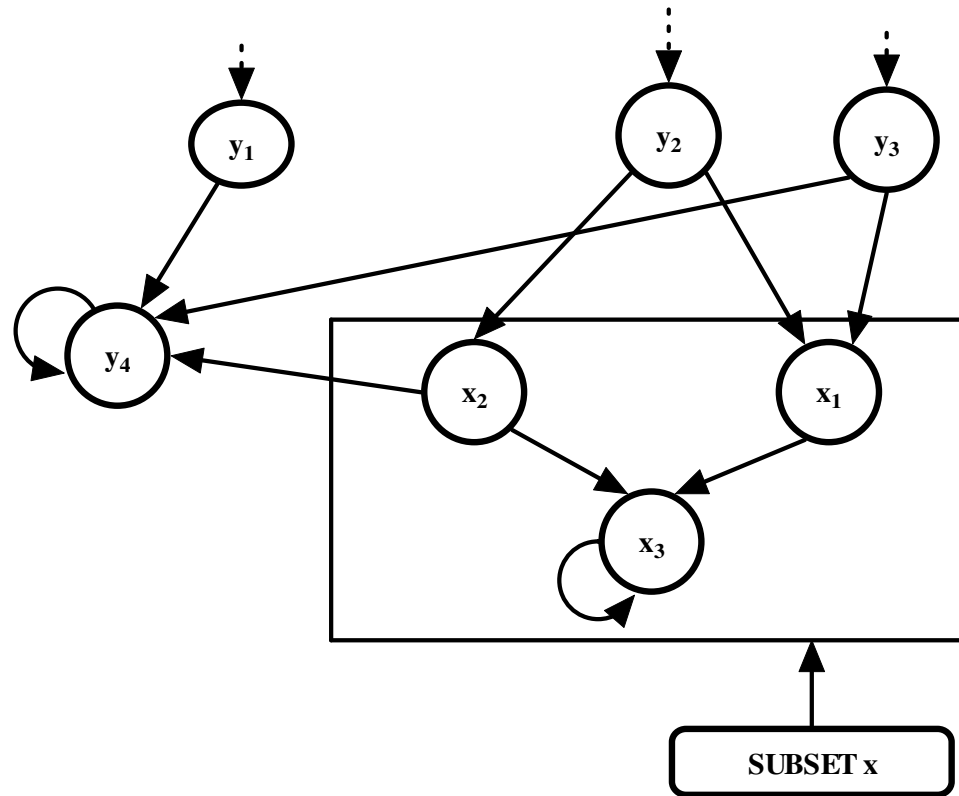
- To find the predecessor states, we define two functions:
 - $\text{Pre}_{\exists}(X)$: takes a subset X of states S and return the set of states which can make a transition into X .
 - $\text{Pre}_{\forall}(X)$: takes a subset X of states S and return the set of states which can make a transition **only** into X .

Symbolic Model Checking

$$\text{Pre}_{\exists}(X) = \{s \in S \mid \exists s', (s \rightarrow s' \text{ and } s' \in X)\}$$

$$\text{Pre}_{\forall}(X) = \{s \in S \mid \forall s', (s \rightarrow s' \text{ and } s' \in X)\}$$

Symbolic Model Checking



Symbolic Model Checking

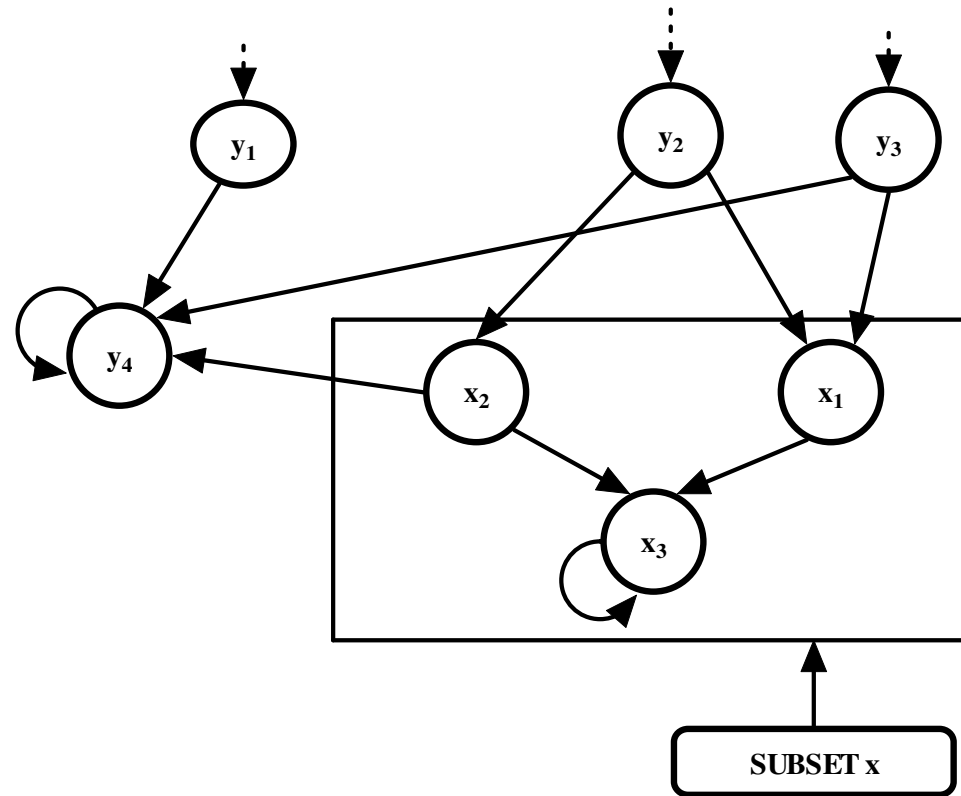
- Important relationship between $\text{Pre}_{\exists}(X)$ and $\text{Pre}_{\forall}(X)$:

$$\text{Pre}_{\forall}(X) = S - \text{Pre}_{\exists}(S - X)$$

S: Set of all states

X: Subset of S

Symbolic Model Checking



Transition System: Represented by ROBDD
Subset X: Represented by ROBDD

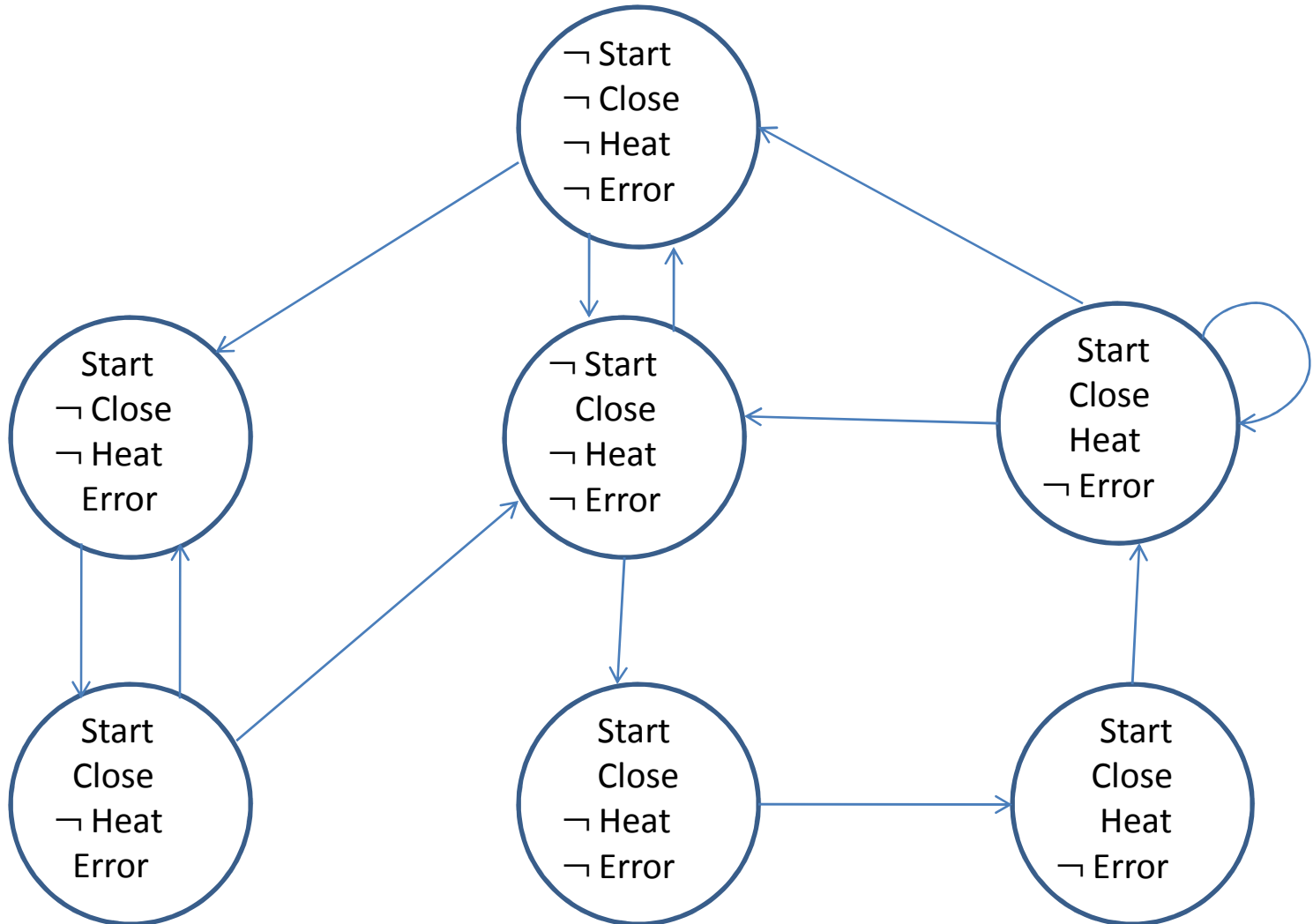
Question

- Draw the state transition diagram of MOD-6 counter.
 - Give a binary encoding to the states
 - Give the Boolean expression for the transition system
 - Indicate the labeling function

Question

- Consider the microwave oven controller and give the state encoding. What is the Boolean expression for the state transition diagram.

Question



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Module VI: Binary Decision Diagram

Lecture V: Symbolic Model Checking

Symbolic Model Checking

- Represent the transition systems with ROBDD
- Set of states can be represented by ROBDD

Symbolic Model Checking

- Basis of Model Checking
 - Graph Traversal algorithms
 - Need to find the predecessor states of a given state or a set of states

Symbolic Model Checking

Symbolic Model Checking

- Important relationship between $\text{Pre}_{\exists}(X)$ and $\text{Pre}_{\forall}(X)$:

$$\text{Pre}_{\forall}(X) = S - \text{Pre}_{\exists}(S - X)$$

S: Set of all states

X: Subset of S

Symbolic Model Checking

Procedure for $\text{Pre}_{\exists}(X)$

Given,

- B_X : OBDD for set of states X .
- B_{\rightarrow} : OBDD for transition relations.

Procedure,

- Rename the variables in B_X to their primed versions; call the resulting OBDD B_X' .
- Compute the OBDD for $\text{exists}(x', \text{apply}(\bullet, B_{\rightarrow}, B_X'))$ using the **apply** and **exists** algorithms.

Symbolic Model Checking

- Rename the variables in B_x to their primed versions; call the resulting OBDD B_x' .

Symbolic Model Checking

- Compute the OBDD for $\text{exists}(x', \text{apply}(\bullet, B_{\rightarrow}, B_{x'}))$ using the **apply** and **exists** algorithms.

CTL Model Checking

Function $SAT_{EX}(p)$

/* determines the set of states satisfying EXp */

local var X, Y

begin

$X := SAT(p)$

$Y := \{s_0 \in S \mid s_0 \rightarrow s_1 \text{ for some } s_1 \in X\}$

return Y

end

Symbolic Model Checking

EX(B_ϕ):

B_ϕ : OBDD for set of states where ϕ is true.

// Analogous to $X := SAT(\phi)$;

B_{\rightarrow} : OBDD for transition relation.

Return $Pre_{\exists}(B_\phi)$. *// Analogous to $Y := \{s \in$*

$S \mid \text{exists } s', (s \rightarrow s' \text{ and } s' \in X)\}$;

Evaluation of $Pre_{\exists}(X)$

Symbolic Model Checking

CTL Model Checking

Function $SAT_{AF}(p)$

/* determines the set of states satisfying AFp */

local var X, Y

begin

$X := S, Y := SAT(p),$

 repeat until $X = Y$

 begin

$X := Y$

$Y := Y \cup \{s \mid \text{for all } s' \text{ with } s \rightarrow s' \text{ we have } s' \in Y\}$

 end

 return Y

end

CTL Model Checking

Symbolic Model Checking

AF(B_ϕ):

B_ϕ : OBDD for set of states where ϕ is true. // Analogous to “ $Y := SAT(\phi)$ ”;

B_\rightarrow : OBDD for transition relation.

B_X : OBDD for all states of the system. // Analogous to “ $X := S$ ”;

repeat until $B_X = B_\phi$ // Analogous to “Repeat until $X = Y$ ”

$B_X := B_\phi$ // Analogous to “ $X := Y$ ”;

$B_\phi := \text{apply}(+, B_\phi, \text{Pre}_\forall(B_\phi))$ // Analogous to “ $Y := Y \cup \{s \in S \mid \text{for all } s', (s \rightarrow s' \text{ implies } s' \in Y)\}$ ”

end

return B_ϕ

$$\text{Pre}_\forall(X) = S - \text{Pre}_\exists(S - X)$$

CTL Model Checking

Function $SAT_{EU}(p,q)$

/* determines the set of states satisfying $E(p \cup q)$ */

local var W,X,Y

begin

$W := SAT(p), X := S, Y := SAT(q)$

 repeat until $X = Y$

 begin

$X := Y$

$Y := Y \cup (W \cap \{s \mid \text{exists } s' \text{ such that } s \rightarrow s' \text{ and } s' \in Y\})$

 end

 return Y

end

CTL Model Checking

Symbolic Model Checking

$\text{EU}(\mathbf{B}_{\psi_1}, \mathbf{B}_{\psi_2})$:

\mathbf{B}_X : OBDD for all states of the system. // *Analogous to*

“ $X := S$

\mathbf{B}_{ψ_1} : OBDD for set of states where ψ_1 is true. // *Analogous*

to “ $W := \text{SAT}(\psi_1)$;

\mathbf{B}_{ψ_2} : OBDD for set of states where ψ_2 is true. // *Analogous*

to “ $Y := \text{SAT}(\psi_2)$;

\mathbf{B}_{\rightarrow} : OBDD for transition relation.

repeat until $\mathbf{B}_X = \mathbf{B}_{\psi_2}$

$\mathbf{B}_X := \mathbf{B}_{\psi_2}$ // *Analogous to “ $X := Y$;*

$\mathbf{B}_{\psi_2} := \text{apply}(+, \mathbf{B}_{\psi_2}, \text{apply}(\bullet, \mathbf{B}_{\psi_1}, \text{Pre}_{\exists}(\mathbf{B}_{\psi_2})))$ //

Analogous to “ $Y := Y \cup (W \cap \{s \in S \mid \text{exists } s', (s \rightarrow s'$

and } \in Y\}));

end

return \mathbf{B}_{ψ_2}

Tools

- CUDD
 - CU Decision Diagram Package
 - University of Colorado at Boulder
- nuSMV
 - Extension of SMV, the first model checker based on BDD
- SPIN
 - LTL model checker developed at BELL labs

System Design Verification

- Model of the system
 - Kripke structure (kind of FSM)
- Specification
 - Specification language (like CTL)
- Verification Method
 - Model Checking

System Design Verification

- Model Checking Algorithms
 - Polynomial algorithm
 - Method can be easily automated
 - It provides counter example
- Problem with model checking
 - State space explosion problem
- Symbolic Model Checking
 - Use of OBDDs to content the state space explosion problem

Question

- We have discussed system model for
 - Elevator controller
 - Microwave oven controller
- Specification and verification of
 - Traffic light controller
 - Controller for ATM
- Use of tools
 - nuSMV and SPIN

Design Cycle: Digital Systems

- Specification
- **Design**
- **Verification**
- Implementation
- **Testing**
- Installation/marketing
- Maintenance

The Course: Digital VLSI Design

- This course is about Digital VLSI Design
- This course consists of three parts
 - Design
 - Verification
 - Test

